

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Advanced Settings Warning: Setting wrong values in below sections May cause system to malfunction. > CPU Configuration > IDE Configuration > LAN Configuration > Floppy Configuration > SuperIO Configuration > Hardware Health Configuration > Voltage Monitor > ACPI Configuration > Remote Access Configuration > USB Configuration					Configure CPU. <- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY	
Advanced	
Configure advanced CPU settings Manufacturer: Intel Brand String: Intel (R) Pentium(R) M processor 1500M Frequency : 600MHz FSB Speed : 400MHz Cache L1 : 32 KB Cache L2 : 1024 KB Intel(R) SpeedStep(tm) tech. [Maximum Speed]	Depending on AC or Battery powered, CPU speed will change based on the selections. <- Select Screen Select Item +- Change Option F1 General Help F10 Save and Exit ESC Exit
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Feature	Options	Description
Intel(R) SpeedStep(tm) tech.	Maximum Speed, Minimum Speed, Automatic, Disabled	Select the operation mode of the CPU. To ensure full performance of the CPU, use the Maximum Speed setting.

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
IDE Configuration		[P-ATA Only]	Select IDE Mode.				
IDE Configuration		[P-ATA Only]	P-ATA Only: 4 P-ATA & 2 S-ATA				
S-ATA Running Enhanced		[Yes]	S-ATA Only: 2 S-ATA				
P-ATA Channel Selection		[Both]	P-ATA & S-ATA 2 P-ATA & 2 S-ATA				
S-ATA Ports Definition		[P0-3 rd ./P1-4th]					
Configure S-ATA as RAID		[No]					
Primary IDE Master		: [Hard Disk]					
Primary IDE Slave		: [Not Detected]					
Secondary IDE Master		: [Not Detected]					
Secondary IDE Slave		: [Not Detected]					
Third IDE Master		: [Not Detected]					
Third IDE Slave		: [Not Detected]					
Fourth IDE Master		: [Not Detected]					
Fourth IDE Slave		: [Not Detected]					
ATA(PI) 80Pin Cable Detection		[Host & Device]					
P-ATA1 Cable Detection force		[Disabled]					
P-ATA2 Cable Detection force		[Disabled]					
Hard Disk Write Protect		[Disabled]					
IDE Detect Time Out (Sec)		[35]					
			<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit				
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Feature	Options	Description
IDE Configuration	Disable, P-ATA Only , S-ATA Only, P-ATA & S-ATA	Setup the configuration of the hard drive interfaces

When P-ATA & S-ATA mode is selected:

Feature	Options	Description
Combined Mode Option	P-ATA 1st Channel, S-ATA 1st Channel	Setup the configuration of the hard drive interfaces
S-ATA Ports Definition	P0-Master/P1-Slave, P0-Slave/P1-Master	Select physical ports (P0/P1) to be Master/Slave or Slave/Master

When S-ATA only mode is selected:

Feature	Options	Description
S-ATA Ports Definition	P0-1st./P1-2nd., P0-2nd./P1-1st.	Select physical ports (P0/P1) to be 1st./2nd. or 2nd./1st.

When P-ATA only mode is selected:

Feature	Options	Description
S-ATA Running Enhanced Mode	Yes, No	Setup the S-ATA interface to be running in enhanced mode or legacy mode
P-ATA Channel Selection	Primary, Secondary, Both	Setup the active IDE channels
S-ATA Ports Definition	P0-3 rd ./P1-4 th , P0-4 th ./P1-3 rd	Select physical ports (P0/P1) to be 3 rd /4 th or 4 th /3 rd
Configure S-ATA as RAID	No , Yes	Only available when "P-ATA Only" is selected. Note: Install the driver via USB-Floppy connected to USB port 2 (lower conn.)

BIOS SETUP UTILITY

Advanced

Primary IDE Master

```
Device       :Hard Disk
Vendor       :ST340014A
Size         :40.0GB
LBA Mode     :Supported
Block Mode   :16Sectors
PIO Mode     :4
Async DMA    :MultiWord DMA-2
Ultra DMA    :Ultra DMA-5
S.M.A.R.T.   :Supported
```

Select the type of devices connected to the system

```
Type                [Auto]
LBA/Large Mode      [Auto]
Block (Multi-Sector Transfer) [Auto]
PIO Mode            [Auto]
DMA Mode            [Auto]
S.M.A.R.T.         [Auto]
32Bit Data Transfer [Auto]
```

```
<-  Select Screen
||   Select Item
+-   Change Option
F1   General Help
F10  Save and Exit
ESC  Exit
```

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Feature	Options	Description
Type	Not Installed, Auto , CDROM, ARMD	Select the type of device installed
LBA/Large Mode	Disabled, Auto	Enabling LBA causes Logical Block Addressing to be used in place of Cylinders, Heads, and Sectors.
Block (Multi-Sector Transfer)	Disabled, Auto	Select if the device should run in Block mode
PIO Mode	Auto , 0, 1, 2, 3, 4	Selects the method for transferring the data between the hard disk and system memory. The Setup menu only lists those options supported by the drive and platform.

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Floppy Configuration			[With PXE boot]	Select the type of floppy drive connected to the system			
ETH1 Configuration			: 00E0F4000001				
MAC Address			: 00E0F4000001				
ETH2 Configuration			[Enabled]				
MAC Address			: 00E0F4000002				
ETH3 Configuration			[Enabled]				
MAC Address			: 00E0F4000003				
				<- Select Screen Select item +- change option F1 General Help F10 Save and Exit ESC Exit			
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Feature	Options	Description
ETH1 Configuration	Disabled, Enabled , With RPL/PXE boot	Select if you want to enable the LAN adapter, or if you want to activate the RPL/PXE boot rom
ETH2 Configuration	Disabled, Enabled , With RPL/PXE boot	Select if you want to enable the LAN adapter, or if you want to activate the RPL/PXE boot rom
ETH3 Configuration	Disabled, Enabled , With RPL/PXE boot	Select if you want to enable the LAN adapter, or if you want to activate the RPL/PXE boot rom

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Floppy Configuration				Select the type of floppy drive connected to the system			
Floppy A			[Disabled]				
Floppy B			[Disabled]				
				<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit			
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Configure Win627THF Super IO Chipset						Enable onboard Floppy Controller for use at parallel port	
OnBoard Floppy Controller			[Disabled]				
Serial Port1 Address			[3F8/IRQ4]				
Serial Port2 Address			[2F8/IRQ3]				
Serial Port2 Mode			[Normal]				
Parallel Port Mode			[378]				
Parallel Port Mode			[Normal]				
Parallel Port IRQ			[IRQ7]				
ICH SIO Serial Port1 Adresse			[Disabled]				
ICH SIO Serial Port2 Adresse			[Disabled]				
						<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit	
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Feature	Options	Description
OnBoard Floppy Controller	Disabled, Enabled	Enable or disable the Floppy Controller
Serial Port1 Address	Disabled, 3F8/IRQ4, 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3, 3E8/IRQ6, 3E8/IRQ10, 2E8/IRQ11	Select the BASE I/O address and IRQ
Serial Port2 Address	Disabled, 3F8/IRQ4, 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3, 3E8/IRQ6, 3E8/IRQ10, 2E8/IRQ11	Select the BASE I/O address and IRQ
Serial Port2 Mode	Normal, IRDA, ASK IR	Select Mode for Serial Port2
Parallel Port Address	Disabled, 378, 278, 3BC	Select the I/O address for the LPT. NOTE: you cannot enable the floppy controller and parallel port at the same time!
Parallel Port Mode	Normal, Bi-Directional, EPP, ECP	Select the mode that the parallel port will operate in
EPP Version	1.9, 1.7	Setup with version of EPP you want to run on the parallel port
ECP Mode DMA Channel	DMA0, DMA1, DMA3	Select a DMA channel
Parallel Port IRQ	IRQ5, IRQ7	Select a IRQ
ICH SIO Serial Port1 Address	Disabled, 3F8/IRQ4, 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3, 3E8/IRQ6, 3E8/IRQ10, 2E8/IRQ11	Select the BASE I/O address and IRQ
ICH SIO Serial Port2 Address	Disabled, 3F8/IRQ4, 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3, 3E8/IRQ6, 3E8/IRQ10, 2E8/IRQ11	Select the BASE I/O address and IRQ

BIOS SETUP UTILITY							
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Power	Exit
Hardware Health Event Monitoring					Enable Hardware Health Monitoring Device.		
System Temperature			:37°C/98°F				
CPU Temperature			:43°C/109°F				
External Temperature Sensor			:N/A				
Fan1 Speed			:Fail				
Fan Cruise Control			[Disabled]				
Fan2 Speed			:2537 RPM				
Fan Cruise Control			[Thermal]				
Fan Setting			[45°C/113°F]				
Fan3 Speed			:2164				
Fan Cruise Control			[Speed]				
Fan Setting			[2177 RPM]				
					<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit		
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Feature	Options	Description
Fan Cruise Control	Disabled, Thermal, Speed	Select how the Fan shall operate. When set to Thermal, the Fan will start to run at the CPU die temperature set below. When set to Speed, the Fan will run at the Fixed speed set below.
Fan Settings	1406-5625 RPM, 30°-75°C	The fan can operate in Thermal mode or in a fixed fan speed mode

BIOS SETUP UTILITY							
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Power	Exit
Voltage Monitor					Enable Hardware Health Monitoring Device.		
Requested Core			:1.484 V				
VcoreA			:1.431 V				
VcoreB			:1.483 V				
+3.3Vin			:3.290 V				
+5Vin			:4.985 V				
+12Vin			:12.016 V				
-12Vin			:Good				
+5VSB			:5.012 V				
					<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
ACPU Settings						Enable Hardware Health Monitoring Device.	
ACPI Aware O/S			[Yes]				
> General ACPI Configuration > Advanced ACPI Configuration							
						<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit	
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Feature	Options	Description
ACPI Aware O/S	No, Yes	Select if your O/S supports ACPI

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
General ACPI Configuration						Select the ACPI state used for System Suspend.	
Suspend mode			[S1 & S3 (STR)]				
Repost Video on S3 Resume			[No]				
S4BIOS Support			[Disabled]				
						<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit	
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Advanced ACPI Configuration						Enable RSDP pointers to 64-bit Fixed System Description Tables.	
ACPI 2.0 Features			[No] Yes				
ACPI APIC support			[Enabled]				
APIC ACPI SCI IRQ			[Disabled]				
AMI OEMB table			[Enabled]				
Headless mode			[Disabled]				
						<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit	
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Configure Remote Access type and parameters					Enable RSDP pointers to 64-bit Fixed System Description Tables.		
Remote Access		[Enabled]	Disabled				
Serial port number		[ICH COM1]					
Serial Port Mode		[115200 8,n,1]					
Flow Control		[None]					
Redirection		[Always]					
Terminal Type		[ANSI]					
VT-UTF8 Combo Key Support		[Disabled]					
					<- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
USB Configuration					Enables USB host controllers.		
Module Version - 2.24.0-7,4							
USB Devices Enabled :							
1 Drive							
USB Function [All USB Ports]							
Legacy USB Support [Enabled]							
USB 2.0 Controller [Enabled]							
USB 2.0 Controller Mode [HiSpeed]							
> USB Mass Storage Device Configuration					<- Select Screen Select item +- change option F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Advanced PCI/PnP Settings Warning: Setting wrong values in below sections May cause system to malfunction. Plug & Play O/S [No] Yes PCI Latency Timer [64] Allocate IRQ to PCI VGA [Yes] PCI IDE BusMaster [Enabled] PCI Raiser Support [Disabled] Disable Unused PCI Clocks [Auto] Spread Spectrum Mode [Disabled]						NO: lets the BIOS configure all the devices in the system. YES: lets the operating system configure Plug and Play (PnP) devices not required for boot if your system has a Plug and Play operating system. <- Select Screen Select Item +- change option F1 General Help F10 Save and Exit ESC Exit	
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Feature	Options	Description
Plug & Play O/S	No, Yes	Select if you have a PnP O/S
PCI Latency Timer	32, 64, 96, 128, 160, 192, 224, 248	Value in units of PCI clocks for PCI device latency timer register
Allocate IRQ to PCI VGA	Yes, No	Assigns IRQ to PCI VGA card
PCI IDE BusMaster	Enabled, Disabled	Setup PCI bus mastering for read/write to IDE drives
PCI Raiser Support	Disabled, PCI Slot3, PCI Slot2, PCI Slot1	Setup if you are using a PCI Raiser card to get one more PCI Slot (Vertical)
Disable Unused PCI Clocks	Auto, No	Disables PCI clocks if no PCI card is detected
Spread Spectrum	Disabled, Enabled	A technique for spreading the signal bandwidth over a wide range of frequencies to lower Radiated Emission

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Boot Settings					Configure Settings during System Boot.		
> Boot Settings Configuration							
> Boot Device Priority							
Removable Devices 1st					[No]		
					<- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Boot Settings					Configure Settings during System Boot.		
Quick Boot					[Enabled]		
Quiet Boot					[Disabled]		
Bootup Num-Lock					[On]		
PS/2 Mouse Support					[Auto]		
Halt on					[All, But Keyboard]		
Hit 'DEL' Message Display					[Enabled]		
Interrupt 19 Capture					[Disabled]		
					<- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Security Settings Supervisor Password :Installed User Password :Installed Change Supervisor Password User Access Level [Full Access] Change User Password Clear User Password Password Check [Setup] Boot Sector Virus Protection [Disabled] Enabled Hard Disk Security					Install or Change the password. <- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Advanced Chipset Settings Warning: Setting wrong values in below sections may cause system to malfunction. > Intel Montara-GML NorthBridge Configuration > SouthBridge Configuration					Intel Montara-GML NorthBridge chipset configuration options. <- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
Configure advanced settings for NorthBrigde					Select which graphics controller to use as the primary boot device.		
Primary Video Device			[Auto]				
Graphics Mode Select			[Enabled, 8MB]				
IGD - Device 2,Function 1:			[Enabled]				
Boot Type:			[CRT]				
Backlight Signal inversion			[Enabled]				
LCDVCC Voltage			[3.3V]				
LVDS			[Normal]				
DVO			[N/A]				
					<- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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Feature	Options	Description
Primary Video Device	Internal, External PCI, External AGP, Auto	Select witch graphics controller to use as the primary boot device
Graphics Mode Select	Disabled, 1MB, 4MB, 8MB , 16MB, 32MB	Select the amount of system memory used by the internal graphics device
IGD – Device 2, Function 1	Disabled, Enabled	Setup the multimonitor function
Boot Type	VBIOS Default, CRT , LFP, CRT+LFP , EFP, TV, CRT+EFP, CRT+TV, EFP+EFP2, EFP+TV	Setup type of boot screen
Backlight Signal Inversion	Disabled, Enabled	Select the signal polarity
LCDVCC Voltage	3.3V , 5V	Setup the LCD Voltage
LVDS	Panels	Chose the connected LVDS panel
DVO	DVO Chip	Select the DVO connection

BIOS SETUP UTILITY							
Main	Advanced	PCIPnP	Boot	Security	Chipset	Power	Exit
					Enable / Disable the ICH4 IOAPIC function.		
IOAPIC [Enabled] Extended IOAPIC [Enabled] OnBoard AC'97 Audio [Enabled] OnBoard Amplifier [Enabled]					<- Select Screen Select Item Enter Go to Sub Screen F1 General Help F10 Save and Exit ESC Exit		
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Feature	Options	Description
IOAPIC	Disabled, Enabled	Setup the ICHS IOAPIC function
Extended IOAPIC	Disabled, Enabled	Setup the extended mode of ICHS IOAPIC
OnBoard AC'97 Audio	Disabled, Enabled	Setup the onboard audio
OnBoard Amplifier	Disabled, Enabled	Use the OnBoard Amplifier on lineout

BIOS SETUP UTILITY							
Main	Advanced	PCI/PnP	Boot	Security	Chipset	Power	Exit
ADVANCED SMI ENABLE CONTROLS Power Management/APM [Enabled] Power Button Mode [On/Off]						Enable/Disable SMI based power management and APM support.	
ADVANCED RESUME EVENT CONTROLS USB Controller Resume [Enabled] PME Resume [Disabled] RI Resume [Disabled]							
RTC Resume [Enabled] RTC Alarm Data [11] RTC Alarm Time [11:11:11]							
PS/2 Kbd/Mouse S4/S5 Wake [Disabled] S3-S5 Keyboard Hotkey [Any key]						<- Select Screen Select Item +- Change Option F1 General Help F10 Save and Exit ESC Exit	
AC Power Loss Restart [Off]							
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Feature	Options	Description
Power Management/APM	Disabled, Enabled	Setup the SMI/APM support
Power Button Mode	On/Off , Suspend	Select Power button functionality
USB Controller Resume	Disabled , Enabled	Lets the USB devices wake up from sleep state
PME/WOL	Disabled , Enabled	Allow PME/WOL to wake from sleep states
RI Resume	Disabled , Enabled	Allow RI/Modem to wake from sleep states
RTC Resume	Enabled, Disabled	Let the board start up on a specific date and time
RTC Alarm Date	Every Day, 1-31	Setup the date you want the board to start
RTC Alarm Time	HH:MM:SS	Setup the time you want the board to start
PS/2 Kbd/Mouse S4/S5 Wake	Disabled , Enabled	When disabled the board can wake from S1 and S3, and when enabled it can also wake from S4 and S5
S3-S5 Keyboard Hotkey	Any key , Space, Enter, Sleep button	Setup the key that can wake up the board
AC Power Loss Restart	Off , On, Previous State	Select whether or not to restart the system after AC power loss: Off keeps the power off until the power button is pressed. On restores power to the computer. Previous State restores the previous power state before power loss occurred.

BIOS SETUP UTILITY

Main Advanced PCIPnP **Boot** Security Chipset Power Exit

Exit Options

Save Changes and Exit
 Discard Changes and Exit
 Discard Changes

Load Optimal Defaults
 Load Failsafe Defaults

Halt on invalid Time/Date [Enabled]
 Secure CMOS [Disabled] Enabled

Exit system setup after saving the changes.

F10 Key can be used for this operation.

<- Select Screen
 || Select Item
 Enter Go to Sub Screen
 F1 General Help
 F10 Save and Exit
 ESC Exit