

TEWE-ORION-0816-100X

TECHNICAL REFERENCE MANUAL

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Congratulations on your new device! It will supply you with accurate, complete and reproducible measurement results for your decision making. Look forward to the easy handling and the flexible and modular use of your DEWETRON product and draw upon more than 25 years of DEWETRON expertise in measurement engineering.



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MODULAR



IS09001

COMPETENT





COMMITTED

APPROVED

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CE-Certificate of conformity

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For Asia and Europe, please contact:

DEWETRON Ges.m.b.H. Parkring 4 A-8074 Graz-Grambach AUSTRIA Tel.: +43 316 3070 Fax: +43 316 307090 Email: support@dewetron.com Web: http://www.dewetron.com

The telephone hotline is available Monday to Friday between 08:00 and 17:00 CET (GMT +1:00) For the Americas, please contact:

DEWETRON, Inc. 10 High Street, Suite K Wakefield, RI 02879 U.S.A. Tel.: +1 401 284 3750 Toll-free: +1 877 431 5166 Fax: +1 401 284 3755 Email: support@dewamerica.com Web: http://www.dewamerica.com

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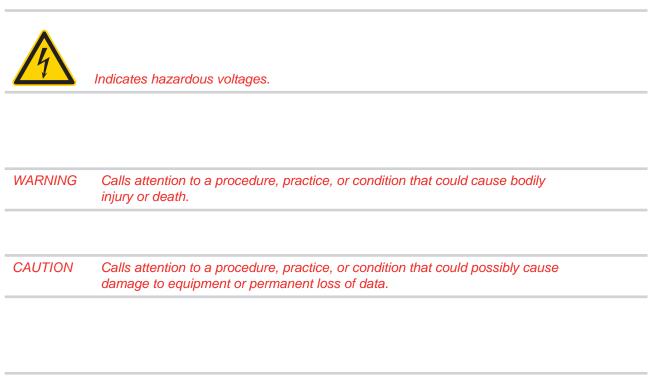
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Safety instructions

Safety symbols in the manual



WARNINGS

The following general safety precautions must be observed during all phases of operation, service, and repair of this product. Failure to comply with these precautions or with specific warnings elsewhere in this manual violates safety standards of design, manufacture, and intended use of the product. DEWETRON Elektronische Messgeraete Ges.m.b.H. assumes no liability for the customer's failure to comply with these requirements.

All accessories shown in this document are available as option and will not be shipped as standard parts.

Safety instructions for all DEWETRON DAQ boards

- The DEWETRON data acquisition boards may only be installed by experts.
- Read your manual before operating the board.
- Observe local laws when using the board.
- DO NOT operate the product in an explosive atmosphere or in the presence of flammable gases or fumes.
- DO NOT operate damaged equipment: Whenever it is possible that the safety protection features built into this product have been impaired, either through physical damage, excessive moisture, or any other reason, REMOVE POWER and do not use the product until safe operation can be verified by service-trained personnel. If necessary, return the product to a DEWETRON sales and service office for service and repair to ensure that safety features are maintained.
- Keep away from live circuits: Operating personnel must not remove equipment covers or shields. Procedures involving the removal of covers or shields are for use by service-trained personnel only. Under certain conditions, dangerous voltages may exist even with the equipment switched off. To avoid dangerous electrical shock, DO NOT perform procedures involving cover or shield removal unless you are qualified to do so.
- DO NOT try to service or adjust the board.
- DO NOT substitute parts or modify equipment.
- Before opening the instrument or computer (experts only) disconnect power!
- Don't touch internal wiring (electrostatic damage is possible).
- Don't use higher supply voltage than specified!
- Use only original plugs and cables for harnessing.
- Safety of the operator and the unit depend on following these rules.
- Using the board for medical applications only at owner's risk

General System Information

Environmental Considerations

Information about the environmental impact of the product.

Product End-of-Life Handling

Observe the following guidelines when recycling a DEWETRON system:

System and Components Recycling

Production of this components required the extraction and use of natural resources. The substances contained in the system could be harmful to your health and to the environment if the system is improperly handled at it's end of life! Please recycle this product in an appropriate way to avoid an unnecessary pollution of the environment and to keep natural resources.



This symbol indicates that this system complies with the European Union's requirements according to Directive 2002/96/EC on waste electrical and electronic equipment (WEEE). Please find further informations about recycling on the DEWETRON web site www.dewetron.com

Restriction of Hazardous Substances

This product has been classified as Monitoring and Control equipment, and is outside the scope of the 2002/95/EC RoHS Directive. This product is known to contain lead.

1 Introduction

1.1 Key features

- 8 simultaneous analog inputs (one ADC/channel)
- 1 MSamples/sec per channel
- Four input ranges from ±1.25 V to ±10 V
- Single ended inputs with remote sense
- Two 32-bit synchronous counter/encoder
- 16 synchronous digital inputs
- 8 digital outputs
- RS-485 interface for control and acquisition

Options:

- Two high speed CAN 2.0B controllers
- 8 additional counter/encoder or 32 TTL inputs (ORION-0816-1002)
- 8 additional counter/encoder or 32 inputs adjustable trigger levels (ORION-0816-1004)

1.2 How to use the manual

- In Chapter one you find an overview about the card-types and possibilities of configurations.
- Chapter two explains how to install the card and connect the sensors.
- The ADC conversion and the counter-functions are described in chapter three.
- All specifications are listed in chapter four.

1.3 Overview

The DEWE-ORION-0816-100x series expands the simultaneous sample A/D-boards from DEWETRON. Even the standard version offers already 8 analog input channels, two 32-bit counter/encoder inputs and 16 digital inputs - 8 of them can also be used for outputs. The onboard RS-485 interface controls DEWETRON signal conditioning amplifiers and can directly acquire data from PAD/EPAD series modules.

For guaranteed synchronous data acquisition with the analog inputs, the DEWE-ORION-0816-100x offers 2 high speed CAN interface channels as an option. The boards can be expanded with 8 additional 32-bit counter/encoder channels.

Model	Analog input channels	Max. sampling rate / channel	Digital input channels	Digital I/O	Ext. Clock	Ext. Trigger	Counter Encoder TTL	Counter Encoder ADJ	CAN
DEWE-ORION-0816-1000	8	1 MS/s	2 (8*)	8	1	1	2	-	-
DEWE-ORION-0816-1001	8	1 MS/s	2 (8*)	8	1	1	2	-	2
DEWE-ORION-0816-1002	8	1 MS/s	10 (40*)	8	1	1	2 + 8	-	-
DEWE-ORION-0816-1003	8	1 MS/s	10 (40*)	8	1	1	2 + 8	-	2
DEWE-ORION-0816-1004	8	1 MS/s	10 (40*)	8	1	1	2	8	-
DEWE-ORION-0816-1005	8	1 MS/s	10 (40*)	8	1	1	2	8	2
* Without using counter inputs									



Options:



DEWE-ORION-0816-1002 with additional inputs



9-pin DSUB connectors for CAN







ORION-0816-SYNC option synchronisation of PC based instruments with DEWE-ORIONxx16 cards

Requirements for using the DEWE-ORION-0816-100x 1.4

To install and use the DEWE-ORION-0816-100x device you need:

channel 0 and 1

- PC with one free PCI slot
- WINDOWS 2000 or XP operating system
- DEWE-ORION-0816-100x board
- DEWE-ORION-0816-100x Technical Reference Manual (shipped with the board or available on www.dewetron.com or ftp.dewetron.com)
- Device driver (shipped with the board)

Recommended options (not shipped with the board):

- Signal connection (e.g. BNC connector box ORION-CB16-BNC)
- DEWESoft 6 (or higher) or other application software

1.5 Unpacking

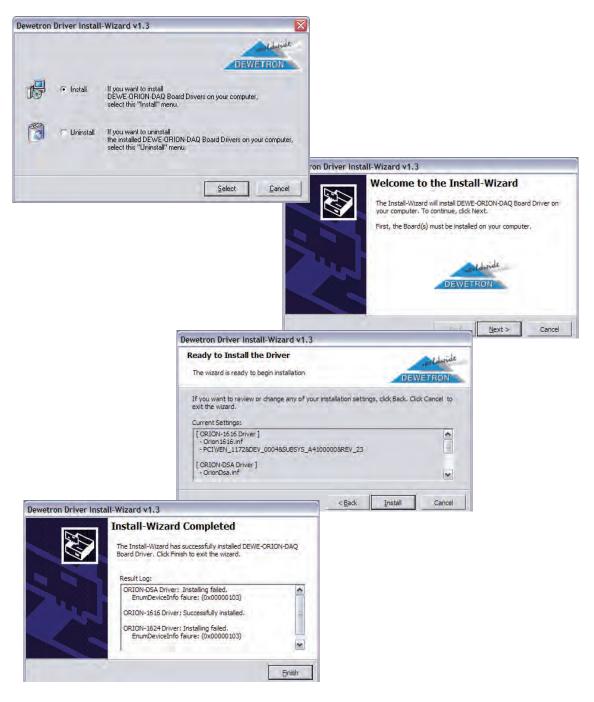
Transport and store the DEWE-ORION-0816-100x in the antistatic plastic package (ESD packaging), where it was originally packed in. Otherwise the device may be damaged by electrostatic discharge. The unpacking and the mounting in your computer should be done in an electrostatic protected area. Don't touch the exposed pins of the connectors! Inspect the device for loose components or other sign of damage before mounting it. Don't install a damaged device into your computer.

2 Using DEWE-ORION-0816-100x

2.1 Hardware installation

Shut down your computer and remove power. Install the board into your computer in correspondence with the instructions in your PC manual. When you have finished the hardware installation and boot up your computer, the operating system will alert that it found a new hardware. Cancel the windows hardware-driver wizard.

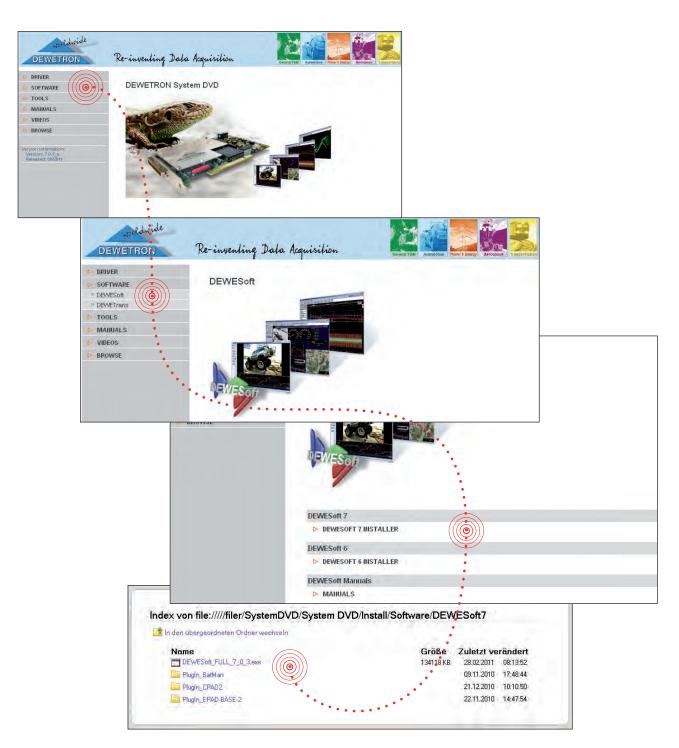
Insert the DEWE-System DVD shipped together with the board into your DVD drive (for example D:\) and start the following executable file: D:\Install\Drivers\6_DaqBoards\Dewetron\OrionDAQ\OrionSetup.exe. After the installation you have to reboot the system.



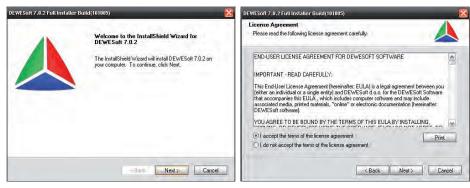
2.2 Software installation

2.2.1 DEWESoft installation

If the installation software doesn't start when you insert the DEWE-System DVD into the computer, start it manually by clicking on the **ShelExec.exe** file on the DVD or navigate to "HTML" and start the **index.html**. Follow the instructions of the installer.



The install shield wizard will simplify the installing procedure.



Select the needed options you want to install and enter your information.

DEWESoft 7.0.2 Full Installer Build(101005)		DEWESoft 7.0.2 Full Installer Build(101005)	DEWESoft 7.0.2 Full Installer Build(101005)	No. 10
Setup Type Select the setup type that best suits your needs.		Choose Destination Location Select folder where setup will install files.	Customer Information Please enter your information.	
Set the type of setup you prefer to install DEWES	oft 7.0.2 .	Setup will install DEW/ESoft 7.0.2 in the following folder.	User Name:	
DEWESoft Measurement Unit	Description	To install to this folder, click Next. To install to a different folder, click Browse and	d se DEWETRON	
Windows Standard	This will install Dewes highest performance a safety. The suggested	another folder.	User Location:	
	procedure is to have all on the second partition separated from system p Therefore in this installati Dewesoft binaries, setup as the data folder is in the folder.		14	
		Destination Folder		
-		D:\DEWESoft7\	Brows	
n Martinett.		11 (15/8) (mkn	(notalB)(alu	
	<back next=""></back>	<back next=""></back>	< Back	Next > Cancel

Select the features you want to install and start the installation.

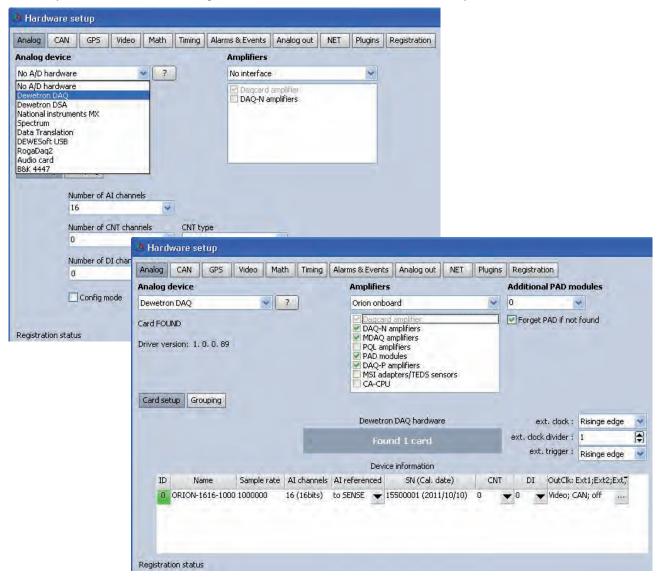




Now DEWESoft is installed on your computer. The software creates some directories on your harddisk.

You can start the software in the Windows start menu or use the icon created on your desktop. For more information about the DEWESoft installation please refer to the *DEWESoft Software Users Manual*.

To modify the hardware settings, select **System** - **Hardware setup** in the menu. Select **DEWETRON DAQ** at the analog device selection field and enter your username, user location and registration code. You can find them in your DEWESoft licence agreement. Be aware that the licence is always related to the hardware.



2.3 Connecting signals

2.3.1 Naming

In multiple board systems a clear defined channel name structure is important to avoid inconstancy in the channel connection. Usually analog input channels are just named in a row. Therefore at a 3 board system the channels are named from CH 0 to CH 23. Due to the flexible structure of the digital and counter inputs the naming in a row is not possible. That's why each input at the ORION base board (code letter B) and also at the expansion board (code letter E) gets a channel prefix name like shown in the example below for a two board system.

Following block diagram explains the naming:

Multi boa	ard system
	ORION-Base-board 0 Source B0_CNT0, Gate B0_CNT0, Aux B0_CNT0 Source B0_CNT1, Gate B0_CNT1, Aux B0_CNT1 B0_DI0 to B0_DI15
	ORION-Expansion-board 0 Source E0_CNT0, Gate E0_CNT0, Aux E0_CNT0 Source E0_CNT1, Gate E0_CNT1, Aux E0_CNT1 E0_DI0 to E0_DI31
	ORION-Base-board 1 Source B1_CNT0, Gate B1_CNT0, Aux B1_CNT0 Source B1_CNT1, Gate B1_CNT1, Aux B1_CNT1 B1_DI0 to B1_DI15
	ORION-Expansion-board 1 Source E1_CNT0, Gate E1_CNT0, Aux E1_CNT0 Source E1_CNT1, Gate E1_CNT1, Aux E1_CNT1 E1_DI0 to E1_DI31

Source B0_CNT0:	Is the source input of CNT0 on ORION-Base-board 0
E1_DI5:	Is the fifth digital input on ORION-Expansion-board 1

2.3.2 Connectors

2.3.2.1 Connector base board

The schematic shows the pin assignment of the input connector. A standard 68-pin high density female type with 0.05 inch pin distance can be used for the signal connection.

The ±15 V output is able to supply up to 50 mA (2 W). The supply is protected against short circuits. Overloading this output may cause an overheating of the DEWE-ORION-0816-100x and lowers the signal quality. The +5 V output can supply up to 500 mA.



Digital Input		_	1	Digital Input
-	+15AV —	35 1	— -15AV	
-	AGND —	36 2	— AGND	-
-	AGND -	37 3	— AGND	-
-	AGND —	38 4	— RES.	-
-	AGND —	39 5	— RES.	-
-	AGND —	40 - 6	— RES.	-
-	AGND —	41 👓 7	— RES.	-
-	AGND —	42 8	— RES.	-
-	AGND —	43 9	— RES.	-
-	AGND —	44 10	— RES.	-
-	RES. —	45 11	— RES.	-
-	AGND —	46 12	— CH7+	-
-	AGND —	47 🗆 🖓 13	— CH6+	-
-	AGND —	48 14	— CH5+	-
-	AGND —	49 15	— CH4+	-
-	AGND —	5016	— CH3+	-
-	AGND —	51 17	— CH2+	-
-	AGND —	52 18	— CH1+	-
-	AISENSE1 -	5319	— CH0+	-
	AGND —	54 - 20	- AGND	-
Bx_DI 8	Bx_DO 0	55 - 21	 — Source Bx_CNT0 	Bx_DI 0
Bx_DI 9	Bx_DO 1	5622	— Gate Bx_CNT0	Bx_DI1
Bx_DI 10	Bx_DO 2	57 - 23	— Aux Bx_CNT0	Bx_DI 2
Bx_DI 11	Bx_DO 3	58 - 24	— Source Bx_CNT1	Bx_DI 3
Bx_DI 12	Bx_DO 4	59 - 25	Gate Bx_CNT1	Bx_DI 4
Bx_DI 13	Bx_DO 5	60 - 26	- Aux Bx_CNT1	Bx_DI 5
3x_DI_14	Bx_DO 6	61 - 27	— RS-485A	-
Bx_DI 15	Bx_DO 7	62 - 28 63 - 29	— RS-485B	
-	DGND -		_ ·	Bx_DI 6 Bx DI 7
-	DGND -	64 30 65 31	EXT CLK	5,_017
-	+5DV	66 32	EXT_CLK EXT_Trigger	-
_	DGND —	67 == 33	- EXT_IIIggei	-
	DGND -	67 == 33 68 == 34	- EXT_CLK2	-
-	DGND	00 = = 34		-

Bx_ Bx_ Bx_[Bx_[Bx_[

Bx_ Bx D

> 68-pin Amplimite series (AMP: 174339-5) SCSI II

2.3.2.2 Adapters for base board

For easy access to the digital and counter inputs DEWETRON instruments are equipped with a DB37 connector for board 0. Also all hardware synchronisation signals (for example clocking the camera, syncronisation external 3rd party hardware, external trigger...) are available on this connector.



Digital Input	Counter Input			Counter Input	Digital Input
	Input Bx_D0 0 Bx_D0 1 Bx_D0 2 Bx_D0 3 Bx_D0 4 Bx_D0 5 Bx_D0 6 Bx_D0 7 EXT_CLK RS-485A EXT_Tigger Source Bx_CNT0 EXT_CLK2 Aux Bx_CNT0 Source Bx_CNT1 EXT_CLK1	2 • 3 • 4 • 5 • 6 • 7 • 8 • 9 • 10 • 11 • 12 • 13 • 14 • 15 • 14 • 15 • 16 • 17 • 17 • 17 • 17 • 17 • 17 • 17	 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 		
-	+5 DV	19•	• 30 • 37	- DGND	-

37-pin SUB-D connector

Optional ORION-CNT2-LEMO

As an option (ORION-CNT2-LEMO) the two counters can be also wired to a 7-pin female lemo connector for direct connection of encoders or any other counter channel source.

- 1: Source Bx CNT(n)
- 2: Aux_Bx_CNT(n)
- 3: Gate Bx_CNT(n)
- 4: Power GND
- 5: +5 V (max. 500 mA)
- 6: +12 V (max. 500 mA)
- 7: Signal GND
- n.. channels 0 to 1 of counter board

Optional ORION-BASE-DI-ISO

6 2 5

7-pin female LEMO connector EGG1B307CLL

Digital Input

Counter Input

Some applications require isolated digital inputs. This can be achieved with the option ORION-BASE-DI-ISO. All inputs are isolated from the input to the ORION board but also isolated between each other.

*) 12V only available on instruments! N.c. on DEWE-5x-PCI-xx expansion systems.

		\sim		
Bx_DI 0	Source Bx_CNT0	1•	•20	In- (DI 0)
Bx_DI 1	Gate_Bx_CNT0	2•	•20	In- (DI 0)
Bx_DI 2	Aux Bx_CNT0	3•		
Bx_DI 3	Source Bx_CNT1	4•	•22	
Bx_DI 4	Gate_Bx_CNT1	5●	•23	
Bx_DI 5	Aux Bx_CNT1	6•	•24	
Bx_DI 6		7•	•25	
Bx_DI 7	. —	8•	•26	
Bx_DI 8	. —	9•	•27	In- (DI 7)
Bx_DI 9		10•	•28	In- (DI 8)
Bx_DI 10	. —	11 •	•29	In- (DI 9)
Bx_DI 11	. —	12•	•30	In- (DI 10)
Bx_DI 12		13•	•31	In- (DI 11)
Bx_DI 12 Bx_DI 13	·	14•	•32	In- (DI 12)
-	· ·	15•	•33	In- (DI 13)
Bx_DI 14	-	16•	•34	In- (DI 14)
Bx_DI 15		17•	•35	In- (DI 15)
-		18•	•36	· ```
-	+5 V		•37	- DGND
-	+12 V*	19•		,

Optional ORION-0816-CB8-BNC

Connection box for ORION-0816-100x, 1 m cable length, 8 BNC for analog inputs

2.3.2.3 Connectors for TTL counter expansion:

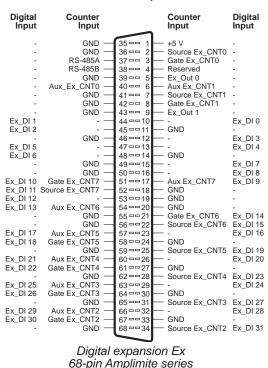
The DEWE-ORION-0816-1000 can be expanded with 8 additional counters or 32 digital inputs with TTL input level. These boards are named with

DEWE-ORION-0816-1002 (without CAN) or DEWE-ORION-0816-1003 (with CAN)

The pin assignment is shown in the schematic:

ORION expansion board connector (68-pin amplimite series)





68-pin Amplimite series (AMP: 174339-5) SCSI II

2.3.2.4 Signal connection for TTL counter expansion

Without any option the counter or digital inputs are wired to a 68 pin female connector on DEWETRON instruments. An easy sensor connection possibility is given with ORION-CB-CNT8 connection box. For direct connection of the sensor to the instrument various options for counter panels are available:

ORION-DIO-PANEL-1

These panels should be used if mainly counter/encoder signals will be measured. All eight counters are wired to 7-pin Lemo connectors and Ex_DI 0 to Ex_DI 15 are connected to a DB37.

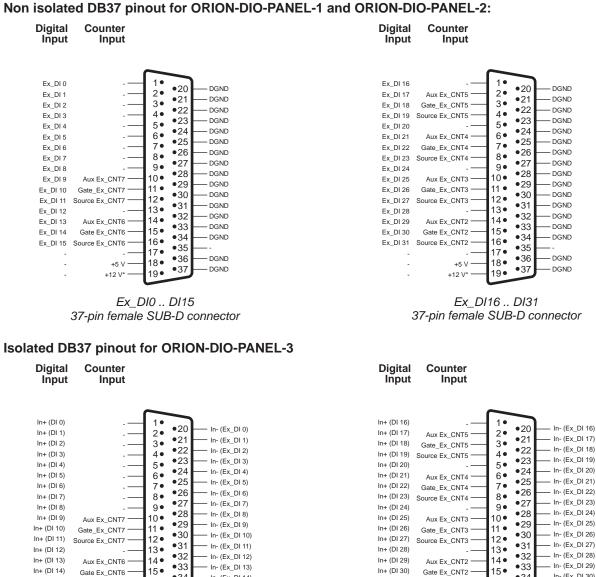
ORION-DIO-PANEL-2

All 32 digital inputs are wired to two DB37 connectors. Ex_CNT 0 and Ex_CNT 1 are connected to a 7-pin Lemo. Therefore this panel is suitable if mainly standard digital inputs are requested.

ORION-DIO-PANEL-3

This option is similar to Panel-2 except the digital inputs are galvanically isolated. The two counter inputs are not isolated.

Counter expansion



•33 In- (Ex_DI 13) 15• In+ (DI 30) Gate Ex CNT2 •34 In- (Ex_DI 14) 16• In+ (DI 31) Source Ex CNT2 •35 - In- (Ex_DI 15) 17• •36 18• +5 V - DGND •37 19 +12 V* Ex_DI0 .. DI15 Ex_DI16 .. DI31 37-pin female SUB-D connector 37-pin female SUB-D connector

*) 12V only available on instruments! N.c. on DEWE-5x-PCI-xx expansion systems.

Gate Ex CNT6

+5 V

+12 V*

Source Ex CNT6

In+ (DI 15)

•34

•35

•36

•37

16•

17•

18•

19•

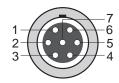
In- (Ex DI 30)

- In- (Ex DI 31)

DGND

Counter/Encoder connector for all panel options

- 1: Source Ex CNT(n)
- 2: Aux_Ex_CNT(n)
- 3: Gate Ex_CNT(n)
- 4: Power GND
- 5: +5 V (max. 500 mA)
- 6: +12 V (max. 500 mA)
- 7: Signal GND
- n.. channels 0 to 1 of counter board



7-pin female LEMO connector EGG1B307CLL

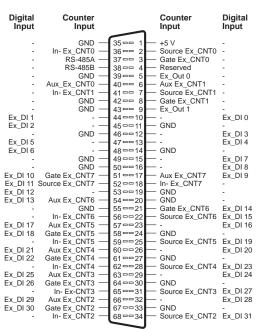
2.3.2.5 Connectors for adjustable counter expansion

The DEWE-ORION-0816-1000 can be expanded with 8 additional counters or 32 digital inputs with differential input, adjustable trigger inputs and programmable AC/DC coupling. These boards are named with

DEWE-ORION-0816-1004 (without CAN) or DEWE-ORION-0816-1005 (with CAN)

The pin assignment is shown in the schematic: ORION expansion board connector (68-pin amplimite series)





Counter expansion

Digital expansion Ex 68-pin Amplimite series (AMP: 174339-5) SCSI II

2.3.2.6 Adapters for adjustable counter expansion

Without any option the counter or digital inputs are wired to a 68 pin female connector on DEWETRON instruments. An easy sensor connection possibility is given with ORION-CB-CNT8 connection box. For direct connection of the sensor to the instrument various options for counter panels are available:

ORION-DIO-PANEL-1

These panels should be used if mainly counter/encoder signals will be measured. All eight counters are wired to 7-pin Lemo connectors and Ex DI 0 to Ex DI 15 are connected to a DB37.

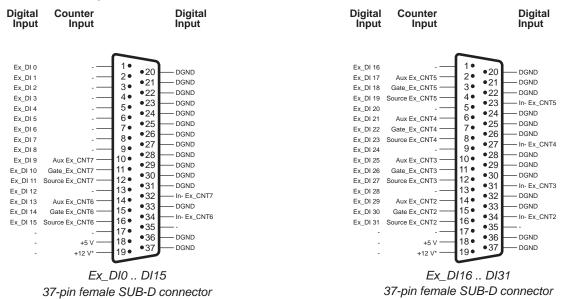
ORION-DIO-PANEL-2

All 32 digital inputs are wired to two DB37 connectors. Ex_CNT 0 and Ex_CNT 1 are connected to a 7-pin Lemo. Therefore this panel is suitable if mainly standard digital inputs are requested.

ORION-DIO-PANEL-3

This option is similar to Panel-2 except the digital inputs are galvanically isolated. The two counter inputs are not isolated.

Non isolated DB37 pinout for ORION-DIO-PANEL-1 and ORION-DIO-PANEL-2



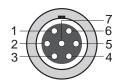
Isolated DB37 pinout for ORION-DIO-PANEL-3 (optional)

Digital Input	Counter Input		, Digital Input		
In+ (DI 11) Sc In+ (DI 12) In+ (DI 13) In+ (DI 14)		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	In+ (DI 16) In+ (DI 17) In+ (DI 18) In+ (DI 19) In+ (DI 21) In+ (DI 22) In+ (DI 23) In+ (DI 23) In+ (DI 24) In+ (DI 24) In+ (DI 25) In+ (DI 26) In+ (DI 27) In+ (DI 28) In+ (DI 28) In+ (DI 30) In+ (DI 31)	Aux Ex_CNT5 24 Gate_Ex_CNT5 34 Source Ex_CNT5 44 Gate_Ex_CNT5 56 Aux Ex_CNT4 76 Gate_Ex_CNT4 76 Gate_Ex_CNT3 100 Gate_Ex_CNT3 100 Gate_Ex_CNT3 120 Aux Ex_CNT3 120 Source Ex_CNT3 120 Gate_Ex_CNT3 120 Gate_Ex_CNT	•21 In-(Ex_DI 17) •22 In-(Ex_DI 18) •23 In-(Ex_DI 19) •24 In-(Ex_DI 20) •25 In-(Ex_DI 21) •26 In-(Ex_DI 22) •27 In-(Ex_DI 23) •28 In-(Ex_DI 24) •29 In-(Ex_DI 25) •30 In-(Ex_DI 27) •31 In-(Ex_DI 27) •32 In-(Ex_DI 27) •33 In-(Ex_DI 28) •34 In-(Ex_DI 30) •35 In-(Ex_DI 31) •36 - •27 DGND
3	—	0 DI15 SUB-D connector		Ex_DI16 37-pin female SU	

*) 12V only available on instruments! N.c. on DEWE-5x-PCI-xx expansion systems.

Counter/Encoder connector for all panel options

- 1: Source Ex_CNT(n)
- 2: Aux_Ex_CNT(n)
- 3: Gate Ex_CNT(n)
- 4: Power GND
- 5: +5 V (max. 500 mA)
- 6: +12 V (max. 500 mA)
- 7: In- Ex_CNT(n)
- n .. channels 0 to 1 of counter board



7-pin female LEMO connector EGG1B307CLL







DEWE-ORION-0816-1004 with ORION-EXP-CNT8- ADJ board

ORION-DIO-PANEL-1: 8 counter inputs wired to 7-pin LEMO connectors

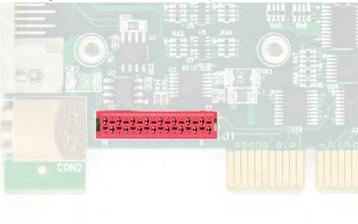
BOX-ORION-CB-CNT8 for easy sensor connection

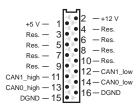


ORION-DIO-PANEL-2: 2 counter inputs wired to LEMO and 32 digital inputs wired to SUB-D connectors

2.3.2.7 Connection of CAN interface

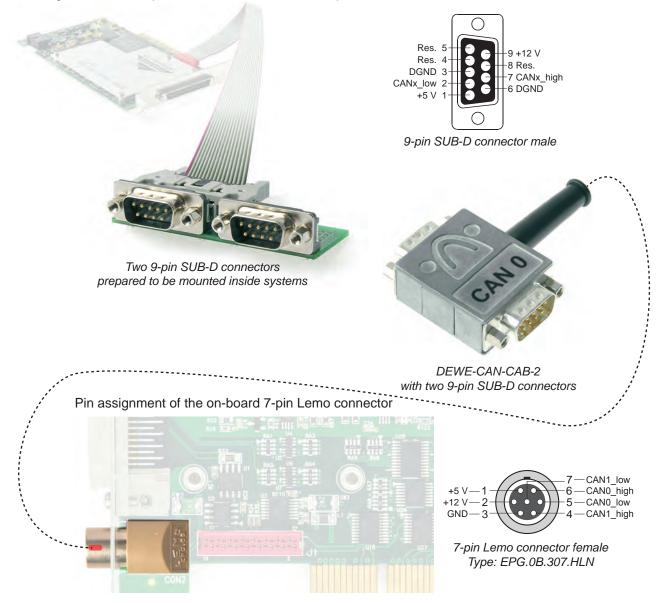
There are two possibilities to connect CAN signals to the board. An adaptor connects the 16-pin Micro Match connector with two 9-pin SUB-D connectors. They are prepared to be mounted inside the system. Pin assignment of the on-board 16-pin Micro Match connector:





16-pin Micro Match connector

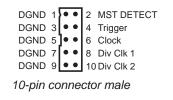
Pin assignment of the 9-pin connectors on the DB9-adaptor.



2.3.2.8 Internal synchronisation connector

For multiple device operation the DEWE-ORION-0816-100x is equipped with an additional synchronization connector. Pin assignment of the on-board 10-pin Synch-connector:





2.4 Analog signals

The block diagram below (figure 1) gives an idea of the analog input configuration. The DEWE-ORION-0816-100x consists of 8 separate input amplifiers and analog to digital converters. Each input has four different gains for getting the input voltage range from ± 1.25 V to ± 10 V.

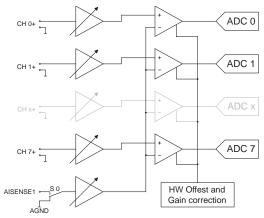


Figure 1: Analog input configuration

The configuration can be changed between single ended (referenced to AGND) and sensed (referenced to sense) mode. The sensed mode is similar to a differential input. The difference to real differential input is that there is just one minus input available in sensed measurement mode. With this technique the influence of ground loops between signal source and the DEWE-ORION-0816-100x can be completely eliminated.

Figure 2 shows the principle of the sensed mode configuration. Usually the level of the signal source is referenced directly to the minus point, which is usually its power supply ground. On the other hand without sensed configuration the DEWE-ORION-0816-100x is also referenced to its power supply ground. Due to the supply current and the line resistance of the signal source and the AD board the ground of both is different. The result of this chain would be an offset error.

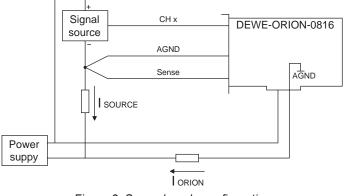


Figure 2: Sensed mode configuration

In the sensed configuration the reference voltage is taken directly at the minus (or power ground) of the sensor to remove this offset error.

Note: In sensed configuration the input range off all channels have to set to the same value!

The input voltage range in sensed mode is limited to ± 12 V. The voltage level of the sense input is the same like the current set input voltage range.

Example for input range ±5 V

Ch x = +8 V, Sense = 4 V, voltage difference = 4 V, result is valid

Ch x = +10 V, Sense = 6 V, voltage difference = 4 V, result is not valid (the input voltage is less than 12 V but the sense voltage level (6 V) is higher than the input range.

If you use a customized cable for signal connection, we strongly recommend using twisted pair cables. Although the single ended configuration each channel has to get its own twisted pair with signal and AGND. Otherwise the high channel to channel isolation (channel cross Talk) get lost. Also the high signal to noise ratio can only be guaranteed by using a shielded twisted pair cable to connect the signal sources to the DEWE-ORION-0816-100x.

2.5 Counter and digital I/O

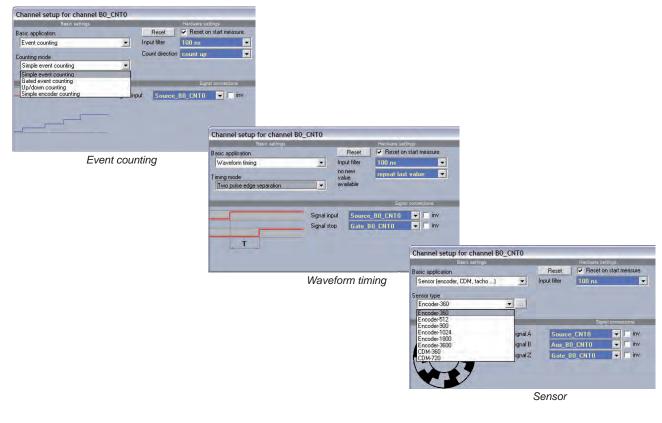
2.5.1 General functionality

The DEWE-ORION-0816-100x is also suited with synchronous 32-bit counters and digital inputs. The flexible signal routing allows easy signal connection and the usage of the same input pin for all counter input functions and for digital inputs. Some pins also can be used for digital output.

In addition to the basic counter function like simple event counting, up/down counting and gated event counting also period time, pulse width, two-edge separation, frequency and all encoder measurements are supported.

2.5.1.1 DEWESoft settings

In Dewesoft there are three basically functions implemented:



2.5.2 Basic counter organisation

Figure 3 shows the principal of a counter block. The counter consists of 4 inputs. The input "Armed" is needed for starting and triggering the counter. The basic input of a counter is the source pin. The default usage of this input is event counting. In addition to the gated counting mode the "GATE" input is also the standard input for period measurement. The AUX input is for special functions like up/down counting or encoder measurement.

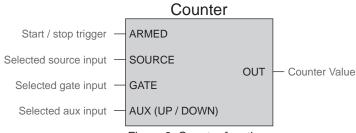


Figure 3: Counter functions

The counters at DEWE-ORION-0816-100x cards are organized in pairs. The pins of each pair can be routed to the counter block of the pair counter. This gives the possibility to measure with just one input the frequency AND also counting the pulses. Also additional pins can be routed to the counter blocks. For example the ADC-clock can be used to measure the period time if the board is used in the external clocking mode. In this case also the time information of the measured analog values is not lost.

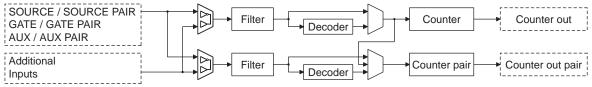


Figure 4: Basic counter organization

All the counter functions are triggered with the rising edge of the input signal. If falling edge trigger is required the inputs can be simple inverted by software settings. To remove glitches at the input a digital filter between 100 nsec and 5 μ sec within 7 steps in the filter block can be selected. The following block selects if the encoder mode is used or not. The pair counter offers an especially selection: It can measure also the input frequency of the neighbor counter.

2.5.3 Counter and DI/O DEWE-ORION-0816-1000/1001

The counters and digital input and output at the DEWE-ORION-0816-1000 can be configured in the most flexible way. DI0 to DI7 can be used as digital input and / or for the counter inputs. Please refer also to the input connector description above.

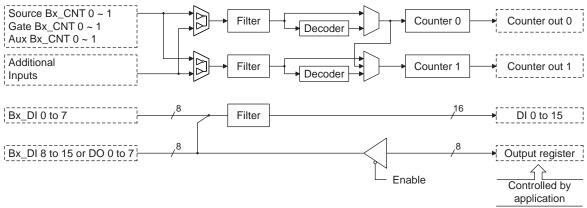


Figure 5: Counter and digital input organization

In addition to the basic counter input selections also several additional inputs can be used as counter source.

Source input	Gate input	AUX input
Source Bx_CNT(n)	Gate Bx_CNT(n)	Aux Bx_CNT(n)
Source from pair counter	Gate from pair counter	AUX form pair counter
Gate Bx CNT(n)	Source Bx CNT(n)	Source Bx CNT(n)
Gate from pair counter	Source from pair counter	Source from pair counter
Aux Bx_CNT(n)	Aux Bx_CNT(n)	Gate Bx_CNT(n)
AUX form pair counter	AUX form pair counter	Gate from pair counter
ADC Clock	ADC Clock	ADC Clock
40 MHz		
Bx_DI6	Bx_DI6	Bx_DI6
Bx DI7	Bx DI7	Bx DI7
EXT_CLK	EXT_CLK	EXT_CLK
EXT_Trigger	EXT_Trigger	EXT_Trigger
EXT_CLK1	EXT_CLK1	EXT_CLK1
EXT_CLK2	EXT_CLK2	EXT_CLK2

The upper 8 bits of the digital input word also can be configured to output. Writing the outputs is an asynchronous action defined by the host application software. But even when if the output mode is activated the output level also can be measured to get the exact timing information when the output is really set.

2.5.4 Counter and DI/O DEWE-ORION-0816-1002/1003

The number of counters and digital inputs can be expanded with the counter expansion add-on board. All the baseboard functionality stays the same like in the standard configuration. Similar to the counter inputs of DEWE-ORION-0816-1000/1001, the input pins can also share the function for digital inputs or counter inputs. Please refer also to the pin-out description in chapter 2.3.1. The block diagram (figure 6) indicates that eight counter channels and theoretically 16 digital inputs can be transferred at once. If higher digital input count is used the counter no. 7 is switched off.

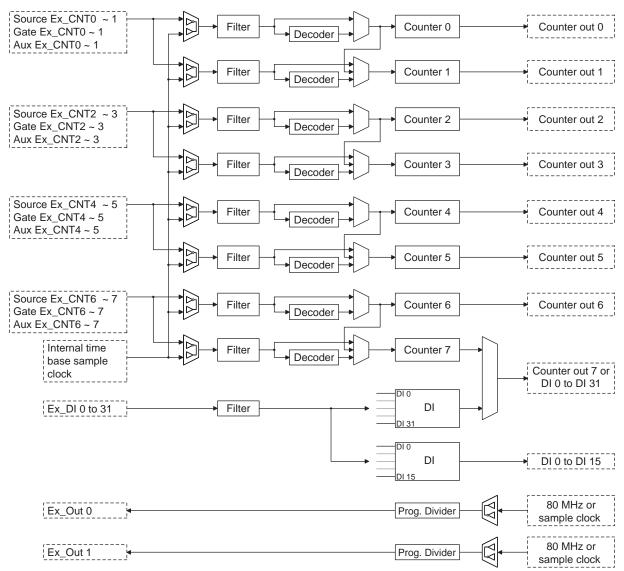


Figure 6: Counter and digital input organization

Source input	Gate input	AUX input
Source	Gate	AUX
Source from pair counter	Gate from pair counter	AUX form pair counter
Gate	Source	Source
Gate from pair counter	Source from pair counter	Source from pair counter
AUX	AUX	Gate
AUX form pair counter	AUX form pair counter	Gate from pair counter
Out0	Out0	Out0
Out1	Out1	Out1
ADC Clock	ADC Clock	ADC Clock
40 MHz		
20 MHz		
10 MHz		

The counter input selection table below shows the input routing possibilities.

There are two output channels available. Each channel can be connected to two input signals via multiplexer (80 Mhz or smaple clock). After the input selection the signal passes the programmable divider, where high time and low time of the signal can be varied. The output channels are activated if the ORION-EXP-CNT8-TTL is in data acquisition mode. Otherwise the output channels are inactive.

The output settings can be done inside the hardware setup of DEWESoft.

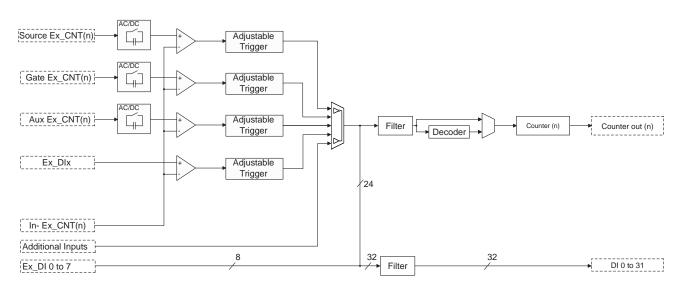
				Devi	ce information			
ID	Name	Sample rate	Al channels	Al referenced	SN (Cal. date)	CNT chnls	DI chnls	OutClk: Ext1;Ext2;Ext7
0	ORION-0816-1003	1000000	8 (16bits)	to GND 🛛 🔫	2570001 (2007/03/06)	2 🔻	0 🔻	Video; Off; Off
0	Expansion CNT	1000000	•	•	15460009	8 🔻	0 🔻	Out0: Off;Out1: Off;



2.5.5 Counter and DI/O DEWE-ORION-0816-1004/1005

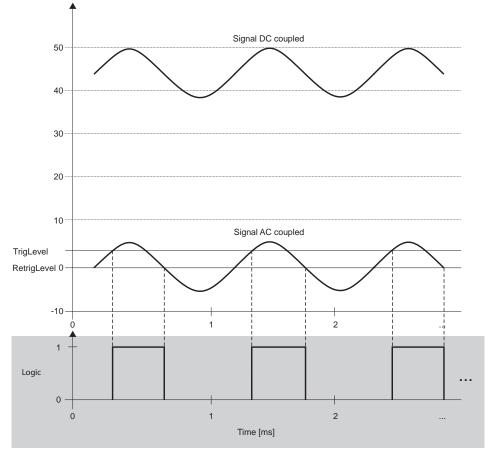
Similar to DEWE-ORION-0816-1002/1003 this board provides eight counter channels and up to 32 digital inputs where the input pins can share this functionality.

The difference is just the electrical input circuit. The DEWE-ORION-0816-1004/1005 provides high voltage differential inputs protected up to 100 Vpeak with a common minus input (for example In- Ex_CNT0) for each counter. Furthermore a programmable trigger and re-trigger level of each input, from 0 to 40 Volt and a software selectable AC/DC coupling with 1 Hz cut off frequency, is included.



With the adjustable counter expansion ORION-EXP-CNT8-ADJ it is possible to set the trigger level and the Retrigger level between 0 and 40 V with a resolution of 40 mV. If the input signal exceeds the value of the trigger level the logical value will be "HIGH" and if the input signal falls below the Retrigger level the logical value is "LOW". As an additional feature the input can be also set to AC coupling for removing the DC component of the input signal.

The diagram below illustrates the functionality of the settable trigger levels.



The levels can be set for each input independent like shown on the next page. Some common used levels are predefined (like TTL, inductive pick-up sensors...). Selecting "Custom" as the trigger type. All parameters are free definable.

Basic settings		Hardware set	tings			Sensor settings	
asic application	Reset	Reset on	start measure	Encoder pu	ulses 360		
Event counting	Input filter	100 ns		Encoder m		-	
ounting mode				Encoder m			
Basic encoder counting				21100001 20			
Advanced counter mode							
		Signa	l connections		_		
	Trigg	er settings same	e for all lines	Trig type:	Coupling:	TrigLevel [V]:	ReTrigLevel [V]:
Signal /	Source	BO_CNTO	👻 🥅 inv	Custom 💌	AC 💌	4	0
Signal E	Aux B	D_CNTO	▼	TTL/CMOS 💌	DC 👻	2	0.8
Signal 2	Gate F	O CNTO	▼ inv	Inductive 💌	AC -	1	0.4
44 . 1	-			Find TrigLevles			
V.14				rind Higcevies			
		0.0	out channels				
		000	or chaines				
					-5		revs 5

If the sensor signal is not known, an automatic algorithm for finding the trigger levels is implemented. A sensor like the shown encoder has usually for all outputs the same signal level. Enabling "trigger settings same for all lines" sets automatically the same level to all used inputs.

	Concerning and the		Signa	i connections		-		A construction of the local sectors of the local se
\$			Tigger settings same	for all lines	Trig type:	Coupling	TrigLevel [V]:	ReTrigLevel [V]:
		Signal A	Source_B0_CNT0	👻 🗌 inv	Eustom 💌	AC 📼	4	0
		Signal B	Aux_B0_CNT0	👻 🗆 inv	TTL/CMOS 💌	DC 💌	2	0.0
- 1		Signal Z	Gate_B0_CNT0	👻 🗖 inv	Inductive 💌	AC 💌	1	0.4
Wat			ALL DE LE CALLER		Find TrigLevies			

2.6 Clock and trigger I/O

The DEWE-ORION-0816 allows external triggering for start of the acquisition using the pin EXT_Trigger. The default detection for the trigger signal is the rising edge but can be configured to falling or both edge (change of input signal) detection. Changing the direction to output, the start of acquisition can be indicated.

				Dew	/etro	on DAQ hardware			ext.	clocł	c: Risinge edge	-	
	 use onboard RS4 use onboard RS4 PAD Channels 		nal	Fo	u	nd 1 card			ext. t	trigge	r: Risinge edge Risinge edge		
	TAD Channels			D)evi	ce information					Falling edge Both edges		
ID	Name	Sample rate	Al channels	Al referenc	ed:	SN (Cal. date)	CNT c	chnis	DI d	hnls	O Set to output	_1	
0	ORION-0816-1003	1000000	8 (16bits)	to GND	▼	2570001 (2007/03/06)	2	-	0		Video; Off; Off		
0	Expansion CNT	1000000	·	•		15460009	8	▼	0	▼	Out0: Off;Out1: Off	;	

EXT_CLKx are used as standard for hardware synchronization to DEWE-CAM, CAN or 3rd party hardware. Each output can set individually. The predefined settings are:

Video: The output frequency is automatically set to the frame rate of the selected video device. If no camera is selected, the output is disabled

CAN: The signal is automatically set for hardware synchronisation of NI-CAN device. If no CAN device is selected, the output is disabled

- FAN: This allows to switch off the system FAN during storing the data. This specially feature is important for sound measurement and needs to be ordered separately.
- Fixed: Outputs the entered frequency synchronized to the sample clock oscillator based on a 12.8 MHz divider. The output starts and stops with the acquisition. The selection of "continuous out" allows to output the clock rate also after the acquisition is stopped. Continuous out selection at signal EXT_CLK starts also the acquisition always synchronised with the rising edge of the output.

Note: This output frequency is only synchronized to the sample frequency at the master board

ADC_CLK: Outputs the actual sample frequency. The selection "continuous out" is only allowed at the master board (board 0).



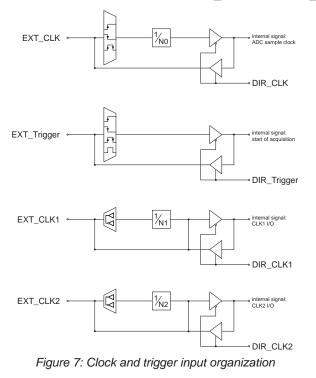
The default settings for the master board are:

EXT_	CLK1:	Video
EXT_	CLK2:	CAN
EXT	CLK:	Off

The output of all slave board is switched off.

	use onboard RS4	185		Dewetro	on DAQ hardware		ext. cloc	k : Risinge edge 💌	
	use onboard RS4		nal	Fou	nd 1 card		ext. trigg	er: Risinge edge 💌	
				Devi	ce information				
ID	Name	Sample rate	Al channels	Al referenced	SN (Cal. date)	CNT chn	ls DI chnis	OutClk: Ext1;Ext2;Ext	Set all clocks to default
0	ORION-0816-1003	1000000	8 (16bits)	to GND 🛛 🔻	2570001 (2007/03/06)	2 🔻	0 🗸	Video; Off; Off	Set all clocks off
0	Expansion CNT	1000000	•	-	15460009	8 🔻	0 🔻	Out0: Off;Out1: Off;	

The diagram below gives an idea of the internal structure of EXT_CLKx and EXT_Trigger circuit:



The direction (input or output) of each pin can be switched separately.

2.7 CAN interface

As an option the DEWE-ORION-0816-1000x also can be suited with two high speed CAN interfaces. Both ports are compatible with CAN 2.0B specification. The CAN transceiver (TI SN65HVD235) has a bus-pin fault protection of up to ±36 V.

The main application for these CAN-ports is acquiring CAN data together with analog data. Although the CAN data is asynchronous to the analog data, the DEWE-ORION-0816-100x series guarantees perfect synchronization. Each incoming CAN message is directly time stamped to the analog sample count number before the data is transferred to the application software.

If just CAN acquisition of CAN messages is required the "Listen Only" mode can be used. In this mode the DEWE-ORION-0816-100x board generates no output data even if the CAN-baud-rate is wrong selected. But this mode is not working using just a direct connection to a sensor. In a point-to-point connection the "Listen Only" mode has to be deactivated at the CAN-Interface. The setup in DEWESoft is shown in the screenshot below.

Analog	CAN	GPS	Video	Math	Timing	Alarms	Analog out	NET
CAN de	vice						Bit order for	display
Dewel	tron DAQ	e	 Card I 	FOUND			70	-
Nation Softing	AN (repla al Instrur g				er version:	1. 0.		
Test C Nation Softing Vector	AN (repla nal Instrur g		Trans		er version: Card type		0. 80 eration mode	1
Test D Nation Softing Vector Dewet Dewet	AN (repla al Instrur g	nents						1

CAN setup in DEWESoft

2.8 RS-485 interface

The DEWE-ORION-0816-100x is suited with an RS-485 interfaced as standard. The baud-rate is fixed to 9600, 8 Data, 1 Stop bit and no parity. This interface is used for configuration of the DAQ and MDAQ signal conditioning modules. Also the acquiring of PAD and EPAD from DEWETRON is possible with this RS-485 port. The interface of the main board and the expansion boards (DEWE-ORION-0816-1002 with counter expansion) is controlled separately.

2.9 Synchronizing multiple boards

2.9.1 Internal synchronization

For multiple device operation the DEWE-ORION-0816-100x is equipped with an additional synchronization connector. A standard 10-pin connector with 1.27 mm flat ribbon cable is available for easy connection between the boards.

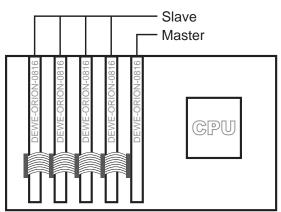


Figure 8: Internal synchronization

2.9.2 Synchronizing multiple systems

If multiple systems or PCI expansion systems are used, a synch-bus amplifier (ORION-xx16-SYNCH) have to be used. This amplifier decouples the internal synch-bus with the external synchronization input and output connector. By changing the internal TTL synch bus levels from TTL to RS-422 level the distance between two systems can be increased by up to 50 metres by using standard CAT5 Ethernet cables.

Please contact DEWETRON if longer synchronization distance is required!

The ORION-xx16-SYNCH also includes the security circuit if two master systems have to be connected together over the synch bus connection. As soon as the system is configured to a master system the external synch is ignored by disabling the SYNCH-IN amplifier. The LED MI (master internal) indicates if the system is configured to a master system. ME (master external) is on, if at the SYNCH-IN connector a valid SYNCH signal is connected.

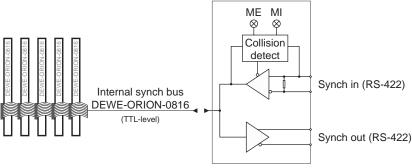
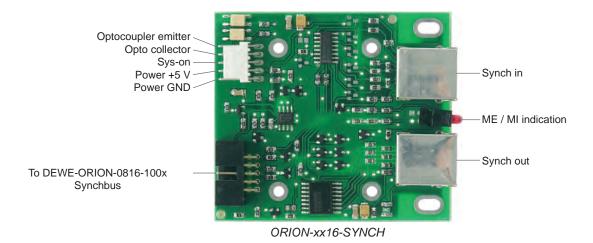


Figure 9: Synchronizing multiple systems



In addition to the synchronization function the ORION-xx16-SYNCH allows also to remote power the slave system. As soon as a master system is present a opto coupler output (PC817) is activated to switch on the power supply. The remote power on also can be controlled with an external control voltage (+5 V@Sys-On).

2.9.3 Defining the board order

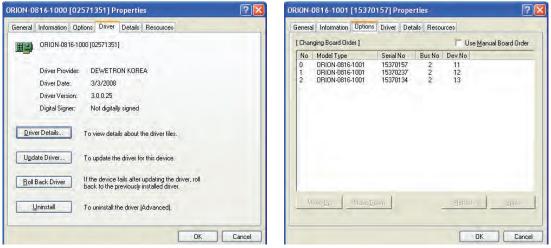
In multiple boards application the location of each single board has to be known. This is required to get the correct channel mapping at the application software. The PCI bus structure allows not a guaranteed board allocation. However, the whole PCI-bus in a PC system is organized in bus and device numbers. The combination of both numbers is unique inside a PCI-bus based system.

By default the DEWE-ORION-0816-100x device driver orders the board beginning with the lowest PCI-bus No. and lowest device number to increasing device No. and PCI-bus No ...

If the system configuration needs to be changed, please modify the settings shown in the next screens. Get this window at Start > Settings > Control Panel > System > Hardware > Device Manager

Bevice Manager	ORION-0816-1001 [153701	57] Properties	? 🛛
Device Manager Device Manager Device Manager Device Manager Device Data Device	ORION-0816-1001 [153701 General Information Options DRION-0016 DEWETRON www.dewetron.com	Driver Details Reso Model Type: Serial No: Calibration Date:	Urces DRION-0816-1001 (0) 15370157 5/3/2006 Al 6; CNT 2; DI 1 bit, CAN 22 1000000 Hz 1 Hz 16 bit
H Storessors Sound, video and game controllers G System devices H G Universal Serial Bus controllers H G Universal Serial Bus controllers			OK Cancel

Device manager



Click on "driver details" to get the next screen

ORION cards in automatic order

The order can be changed by clicking on the name of the board and moving it with the move up/down buttons. Please note that the settings are taken only after clicking the button <Apply>. A click on the <Refresh> button shows the actual settings.

enera		ns Driver Det	ails Resou	lices	CARD C BOCT OF
Chan	ging Board Order]			✓ Use	Manual Board Order
No	Model Type	Serial No	Bus No	Dev No	
)	ORION-0816-1001	15370157	2	11	
2	ORION-0816-1001	15370237	2	12	
	ORION-0816-1001	15370134	2	13	
	ove Up Move	Davis		Refresh	Apply

ORION cards in manual sorted order

3 Theory of operation

3.1 Analog input

3.1.1 Analog input circuit

Figure 10 shows the simplified input circuit of the DEWE-ORION-0816-100x series. The 10 MOhm input termination removes signal floating of not connected input signals. The pre-filter removes noise from the input and protects the PGA against over voltages. This programmable gain amplifier with the steps 1, 2, 4 and 8 allows four different input ranges from ± 1.25 V to 10 V. Especially in sensed mode configuration the differential amplifier removes offset errors caused by ground loops.

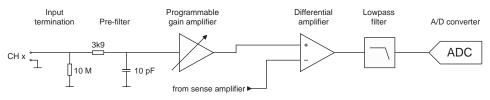


Figure 10: DEWE-ORION-0816-100x input circuit

Please note: In sensed mode the configuration of the input range of all channels has to be the same!

The overall bandwidth is shown below. The analog bandwidth is defined by the following low pass filter.

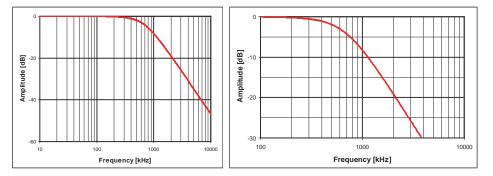


Figure 11: Overall bandwidth of ORION-0816-100x

3.1.2 Analog to digital conversion

The DEWE-ORION-0816-100x series is based on successive approximation analog to digital converter. At each sample the current analog value is tracked by an internal sample & hold circuit for the duration of the conversion time. During this conversion time this analog value is compared bit by bit with the reference voltage for getting the digital value. This architecture allows also multiple board operation like external clocking from 0 samples/sec to the maximum sample rate of 1 MSamples/second.

3.1.3 Output data format

Each analog channel is transferred as a standard twos complement 16-bit value represents the analog input value. 1 LSB equals the whole input range / 2¹⁶. This is possible due to the hardware offset and gain correction at the DEWE-ORION-0816-100x. The PCI-bus width is 32-bits. Because of that the channels are transferred always in pairs to the DMA controller. That means Ch0 is always combined with CH1, Ch2 with Ch3 and so on.

3.1.4 Calibration

Your DEWE-ORION-0816-100x is shipped with a calibration certificate. Typically a recalibration is required every year. The calibration constants are stored in the on-board EEPROM. The calibration can only be done with an optional available calibration kit or you can send the DEWE-ORION-0816-100x back to DEWETRON for recalibration.

3.2 Counter input

3.2.1 Counter applications

As mentioned above each counter block is equipped with three inputs. With this three inputs the following applications can be done:

- Event Counting
- Gated Event Counting
- Up/Down Counter
- Frequency Measurement
- Period Time Measurement
- Pulse Width Measurement
- Two Pulse Edge Separation
- Quadrature Encoder (X1, X2, X4, A-Up/B-Down)

3.2.1.1 Event Counting

In Event Counting the counter counts the number of pulses that occur on counter source. At every sample clock the counter value is read without disturbing the counting process.

Figure 12 shows an example of event counting where the counter counts eight events on *Counter Source*. *Synchronized Value* is the value read by the DEWE-ORION-0816-100x board at *Sample Clock* (encircled numbers in the figure, e.g. (1, 2)).

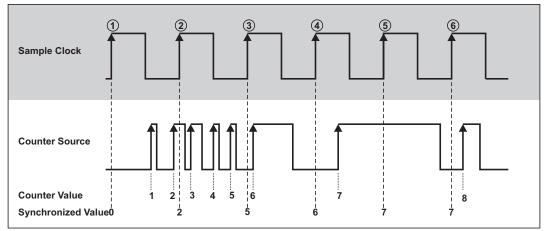


Figure 12: Event Counting

If counting at falling edges is necessary, the input signal has to be inverted. This can be done directly on the ORION-EXP-CNT6 by selecting inverted input.

Channel setup for channel B0_CN	19	b
Basic settings	Hardware settings	Sensor settings
Basic application	Reset Reset on start measure	
Event counting	 Input filter 100 ns 	
Counting mode	Count direction count up	
Basic event counting	Enable signal zero	
Advanced counter mode	and the second s	
	Signal connections	
	ignal input <u>Source_B0_CNT0</u> inv	
	AND REAL PROPERTY OF THE REAL PROPERTY OF	

3.2.1.2 Gated Event Counting

Gated Event Counting is similar to Event Counting except that the counting process is gated. When *Counter Gate* is active, the counter counts pulses which occur on counter source. When *Counter Gate* is inactive the counter retains the current count value. At every *Sample Clock* the value is read.

Figure 13 shows an example of Gated Event Counting where the counter counts three events on *Counter Source*. At ① and ② the counter value is zero, because the gate signal is inactive. At sample clock ③, ④ and ⑤ the actual counter value is read out. At ⑥ the same value as at ⑤ is typed out.

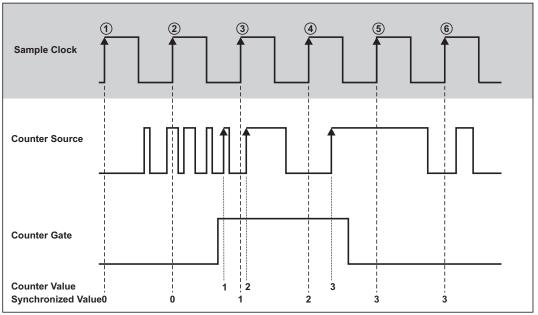


Figure 13: Gated Event Counting

It is also possible, as in Event Counting, to invert the input signals.

Basic settings	A DAY OF A DAY OF A DAY	H	ardware settings				Sensor settings	
asic application		Reset 🔽	Reset on start mea	sure				
Event counting		Input filter	00 ns	-				
ounting mode		Count direction	ount up					
Gated event counting								
Advanced counter mode								
			Signal connection	16	-			
	Signal inp	out Source_B0	CNTO E	inv				
	Signal ga	te Gate BO C	NTO VIC	inv				
			Output channels	1	-	1		-
Unused B0_CNT0	10	0		1	0	-5	0 -	5

3.2.1.3 Up/Down Counter

The Up/Down Counter counts the rising edges on *Counter Source*. The direction of the counting depends on the signal state on *Counter Aux*. If *Counter Aux* is active (high level), the counter is increasing the counter value; if *Counter Aux* is inactive (low level), the counter is decreasing the counter value.

Figure 14 shows Up/Down counting.

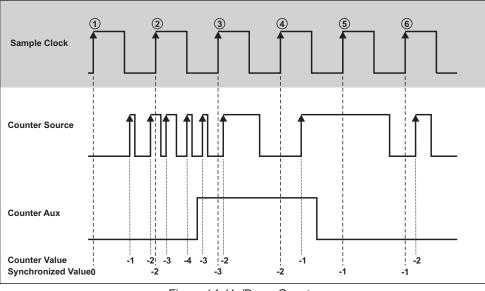
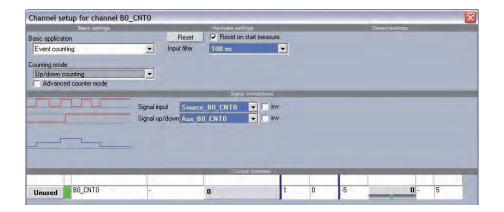


Figure 14: Up/Down Counter



3.2.1.4 Period Time Measurement

In Period Time Measurement the counter uses the internal time base to measure the period time of the signal present on *Counter Source*. The counter counts the rising edges of the internal time base which occurs between two rising edges on *Counter Source*. At the completion of the period interval the counter value is stored in a register and the counter starts counting from zero. At every *Sample Clock* ((1, 2, ..., 6)) the register value is read out.

Figure 15 shows a Period Time Measurement.

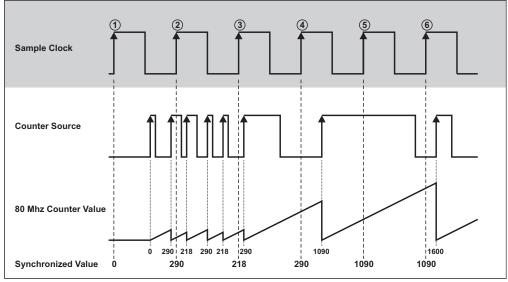


Figure 15: Period Time Measurement

	Basic settings				e settings						
Basic applicat	tion		Reset	Rese	et on start measure						
Waveform I	timing		Input filter	100 ns		+					
iming mode			no new value	repeat	last value	-					
Period, freq	quency	-	available								
		-									
		Signal inp	ut Sourc	:e_BO_CNT	Signal connections 0 💌 🗐 inv						
F	⊃=1/f	Signal inp	ut Sourc	e_BO_CNT							
F		Signal inp	ut Sourc	e_BO_CNT	0 💌 🗖 inv						
Unused		Signal inp	ut Sourc	e_BO_CNT	0 💌 🗖 inv	1	0	0	0 \$	1000	

3.2.1.5 Pulse Width Measurement

In Pulse Width Measurement the counter uses the internal time base to measure the pulse width of the signal present on *Counter Source*. The counter counts the rising edges of the internal time base after a rising edge occurs on counter source. At the falling edge on *Counter Source* the counter value is stored in a register and the counter is set to zero. With the next rising edge on *Counter Source* the counter starts counting again. At every *Sample Clock* (0, 2, ..., 6) the register value is read out.

Figure 16 shows a pulse width measurement.

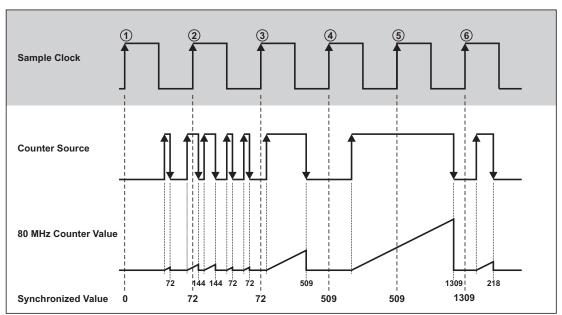


Figure 16: Pulse Width Measurement

For measuring the low time of the signal, the input signal has to be inverted on the ORION-EXP-CNT6.

Basic settings		the second second	Hardware settings		Sensor settings	
asic application		Reset	Reset on start measure			
Waveform timing		Input filter	100 ns 🔫			
iming mode Pulsewidth		no new value available	repeat last value 💉			
PW		-	Signal connections	_		-
FYY	Signal in	put Source	BO_CNTO 💽 🗖 inv			
	_		Gutput channels			

3.2.1.6 Two Pulse Edge Separation Measurement

The two pulse edge separation measurement is similar to the pulse width measurement, except that there are two input signals: *Counter Start* and *Counter Stop*. After a rising edge has occurred on *Counter Start* the counter counts rising edges of the internal time base. Additional edges on signal start are ignored. After a rising edge has occurred on *Counter Stop* the counter stops counting and the value is stored in a register. At the next rising edge on *Counter Start* the counter starts counting from zero again. At every *Sample Clock* ($^{(1)}$, $^{(2)}$, ..., $^{(3)}$) the register value is read out.

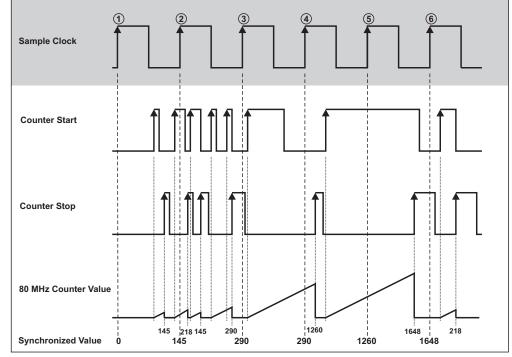
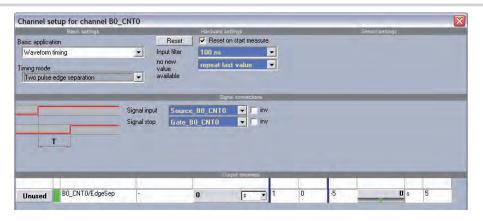


Figure 17 shows an example of Two Pulse Edge Separation Measurement.

Figure 17: Two Pulse Edge Separation Measurement

If the input signals are inverted the counter takes the falling edges for counting.



3.2.1.7 Motion Encoder

Motion encoders have usually three channels: channel A, B and Z. Channel A and channel B are providing the square signals for the counter, and have a phase shift of 90°. With this phase shift the decoder is able to recognize the rotation direction of the motion encoder. The third channel types out one pulse at a certain position at each revolution. This pulse is used to set the counter to zero. The amount of counts per cycle at a given motion encoder depends on the type of decoding: X1, X2, X4. All three types are provided by the ORION-EXP-CNT8-TTL/ADJ. Some motion encoders have two outputs, which are working in a different way. Either channel A or channel B providing the square signal, depending on the direction of the rotation. Also this type is supplied by the ORION-EXP-CNT8-TTL/ADJ.

3.2.1.8 Quadrature Encoder

In the first case X1 decoding is explained. When *Input A* leads *Input B* in a quadrature cycle, the counter increments on rising edges of *Input A*. When *Input B* leads *Input A* in a quadrature cycle, the counter decrements on the falling edges of *Input A*. At every *Sample Clock* (0, @, ..., @) the counter value is read out.

Figure 18 shows the resulting increments and decrements for X1 encoding.

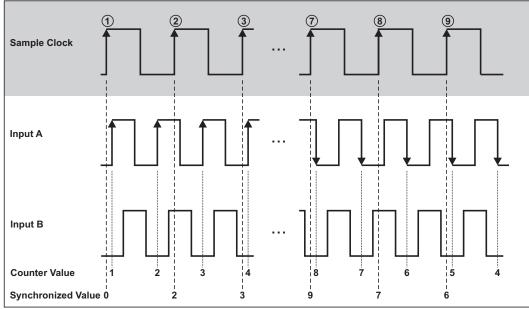


Figure 18: Quadrature Encoder X1 Mode

For X2 encoding the rising edges and the falling edges of *Input A* are used to increment or decrement. The counter increments if *Input A* leads *Input B* and decrements if *Input B* leads *Input A*. This is shown in Figure 19.

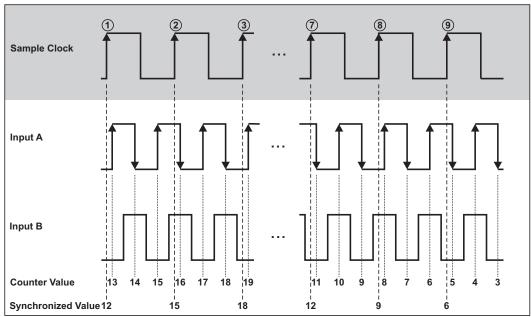


Figure 19: Quadrature Encoder X2 Mode

Similarly, the counter increments or decrements on each edge of *Input A* and *Input B* for X4 decoding. The condition for increment and decrement is the same as for X1 and X2.

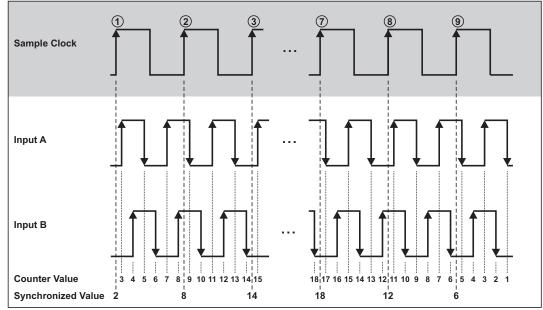


Figure 20 shows the results for X4 encoding.

Figure 20: Quadrature Encoder X4 Mode

The third channel *Input Z*, which is also referred as the index channel, causes the counter to be reloaded with zero in a specific phase of the quadrature cycle.

Figure 21 shows the results for X1 encoding with input Z.

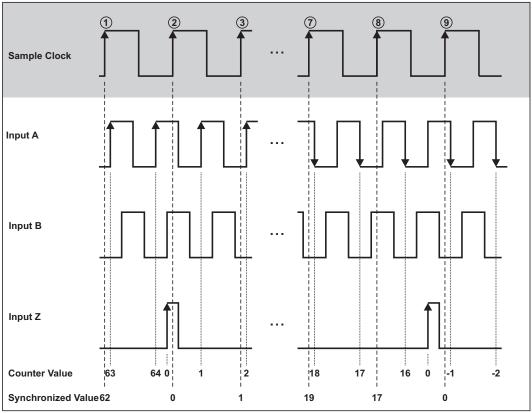


Figure 21: Quadrature Encoder with channel Z

	Basic settings			Hardware set	tings				Sensor settings			
Basic applicati	ion		Reset	Reset or	start measure	61	Encoder pulse	360				
Sensor (enc	coder, CDM, tacho)	💌 İnp	out filter	100 ns		•	Encoder mode	1				
Sensor type							Encoder zero	1 <mark>×1</mark>				
Encoder-36	i0 🗾	m					Encodel zelo	×2 ×4				
1.000					I connections		_	A-Up/	B-Down		_	
	-		-					-	_			
	Sig	gnal A	and the owner where the owner w	BO_CNTO	👻 🗐 inv							
A M	Sig	gnal B	Aux_B0	_CNT0	🛨 🗌 inv							
7 V.W	the second se		And the owner of the owner.									
4	the second se	gnal B gnal Z	Aux_B0 Gate_B0									
4	the second se		And the owner of the owner.									
***	the second se		And the owner of the owner.	D_CNTO	▼ ⊂ inv							
4	the second se		And the owner of the owner.	D_CNTO								
Unused	the second se		Gate_BI	D_CNTO	▼ ⊂ inv		0	5) revs	5	
Unused Unused	Sig		Gate_BI	D_CNT0	vut channels			5	-) revs) RPM	1	
and a second second	B0_CNT0/Angle	gnal Z	Gate_B	0_CNT0 Cut	out channels		0	1	-	BPM	1	

3.2.1.9 A-Up/B-Down Encoder

The A-Up/B-Down Encoder supports two inputs, A and B. A pulse on *Input A* increments the counter on its rising edges. A pulse on *Input B* decrements the counter on its rising edges. At every *Sample Clock* ((0, @, ..., @)) the counter value is read out.

This situation is shown in Figure 22.

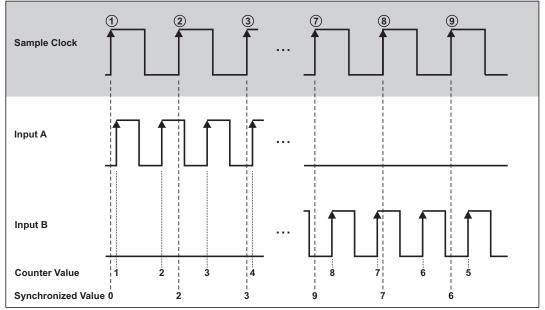


Figure 22: A-Up/B-Down Encoder

Basic settings	State Property and	-	Hardware settings			Ser	soviseltings	
Basic application		Reset	Reset on start	measure				
Event counting		Input filter	100 ns					
Counting mode								
Up/down counting) T							
Advanced counter mode								
			Signal conr	ections	_			
	Signal in	put Source	BO_CNTO	🔲 inv				
	Signal up	down Aux Bt)_CNTO 🔤	🔽 inv				
	_	-	Cutput ch	anneis	-	1		_
Housed B0 CNT0			Cutput ch	anneis	0	-5	0 - 5	1

3.2.1.10 Frequency Measurement

In general it is possible to take the inverse of a period measurement to get the frequency of the input signal. If the period time measurement is done an inaccuracy of counted internal time base cycles of ± 1 cycle appears, because the counted cycles of the internal time base depends on the phase of the input signal with respect to the internal time base. For long period times, and therewith low frequencies, the measurement error is negligible. At high frequencies, and therewith short period times, few cycles are counted. In this case the error of ± 1 cycle becomes significant.

Input Frequency	Number of internal time base cycles	Measurement error of -1 cycle	Measurement error of +1 cycle	Calculated frequency with error of -1 cycle	Calculated frequency with error of +1 cycle
40 kHz	2000	1999	2001	39,98 kHz	40,02 kHz
10 MHz	8	7	9	8,75 MHz	11,25 MHz

Accuracy at period time measurement

For higher precision result the frequency measurement is done with two counters. At the ORION-EXP-CNT8-TTL/ADJ in each case two counters are paired, i.e. it have to be used counter 0 and counter 1 or counter 2 and counter 3 or counter 4 and counter 5 or counter 6 and 7 for the frequency measurement. The first counter counts the rising edges on *Counter Source*. The second counter counts the rising edges of the internal time base. At every rising edge on *Counter Source* the counter value of the second counter is stored in a register. At every *Sample Clock* ((1, (2), ..., (6)) the values of both counters are read out.

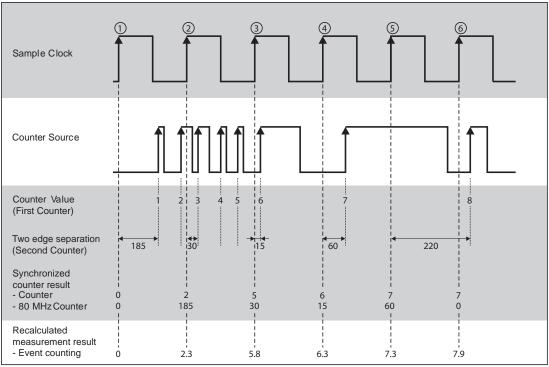
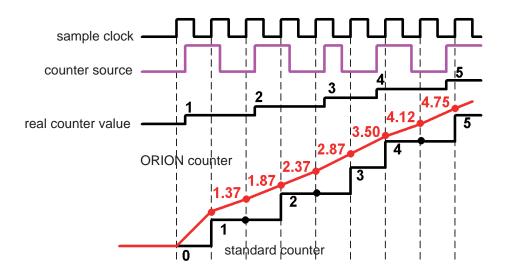


Figure 23: Frequency Measurement

With these both measurement results not only the frequency can be calculated in a precise way. Also the event counter result can be show in fractions because the exact time when the event occurs at the input is known. The event counting result is recalculated with interpolation to the exact sample point like shown in the diagram above.

On the next page, the difference of the measurement result is shown. While a standard counter input shows the value up to one sample delayed, the counter input of the ORION calculates the counter result at the exact sample point.



For low frequency input signals the frequency also can be obtained by measure the period time and take its inverse without more inaccuracy.

3.2.2 Miscellaneous counter functions

3.2.2.1 Filters

Each counter input has a digital filter, which can be set to various gate times. If the gate time is set to "Off", no filter is on the input signal.

The filter circuit samples the input signal on each rising edge of the internal time base. If the input signal maintains his state for at least the gate time, the new state is propagated. As an effect the signal transition is shifted by the gate time.

Figure 24 demonstrates the function of the filter.

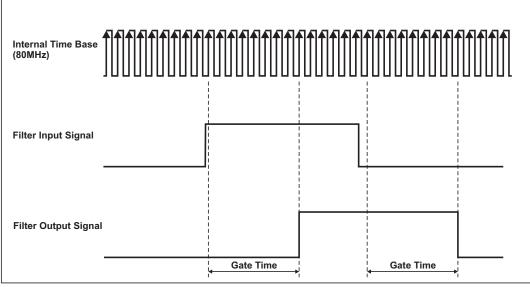


Figure 24: Filters

The intent of the filter is to eliminate unstable states, e.g. glitches, chatter, ..., which may appear on the input signal, as shown in Figure 25.

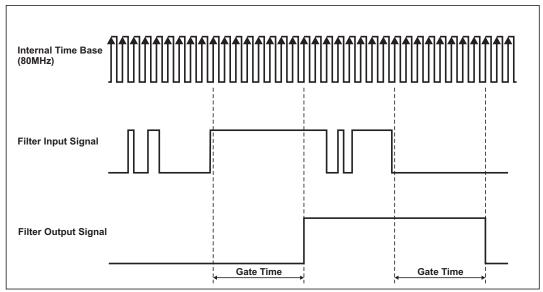


Figure 25: Input signal with chatter

It can be chosen between eight filter settings: Off, 100 ns, 200 ns, 500 ns, 1 μ s, 2 μ s, 4 μ s and 5 μ s. Two examples of filter settings are described.

The 100 ns filter will pass all pulse widths (high and low) that are 100 ns or longer. It will block all pulse widths that are 75 ns or shorter. The 5 μ s filter will pass all pulse widths (high and low) that are 5 μ s or longer and will block all pulse widths that are 4.975 μ s or shorter.

The internal sampling clock (time base) is 80 MHz, so the period time amounts 12.5 ns. Pulse widths between gate time minus two internal time base period times may or may not pass, depending on the phase of the input signal with respect to the internal time base.

Properties of all filter settings:

Filter settings	Pulse width to pass	Pulse width to be blocked
100 ns	100 ns	75 ns
200 ns	200 ns	175 ns
500 ns	500 ns	475 ns
1 µs	1 µs	975 ns
2 µs	2µs	1.975 µs
4 µs	4 µs	3.975 µs
5 µs	5 µs	4.975 µs
Off	-	-

Filter Gate Times

3.2.2.2 Reset on start measure

Usually all counters are reset at the start of data acquisition, i.e. the counter value is set to zero at the start of data acquisition. In some applications this is not required. An angle encoder for example is adjusted to the physical zero position at the beginning of a test procedure. By resetting the counter at every start of the measurement this adjustment get lost. Without this reset the counter is also active if the acquisition is interrupted between the test cycles. As a result the counter types out the absolute angle position at the measurement output all the time.

3.2.2.3 Count Direction

As default setting the count direction is in up-counting mode. Every rising edge at the input will increase the counter value. The DEWE-ORION-0816-100x supports also down counting without the need of an additional input like in the up/down counting mode.

3.2.2.4 No new value available

Especially in every kind of input period time measurement mode (also pulse width or two pulse edge separation measurement) there may be new information between two samples. Also measuring the line frequency of about 50 Hz with a sample rate of 10 kSamples/sec means, that only after every 200th measurement new input frequency information is available. Another example is the measurement on rotating machines if the sensor output frequency is lower than the sample rate. Depending on the application the DEWE-ORION-0816-100x can choose between two different output data settings:

- Repeat last value: last measured cycle time is taken until a new measured cycle time is available.
- Make zero value: as soon as no input information is available the output is set to Zero.

4 Specifications

4.1 Analog input

Analog input		
Channel characteristics		
Number of channels	8 simultaneously sampled	
Input configuration	single ended with remote sen	62
Resolution	16-bit	56
Effectiv number of bits	14,3	
Type of ADC	Successive approximation (S	
Sampling rate	1 S/sec to 1 MS/sec per chan	inei
Sampling rate accuracy	35 ppm	
Input amplifier characteristics		
Input ranges	±1.25, ±2.5, ±5 or ±10 V	
Typical analog bandwidth (-3 dB)	900 kHz	
Input impedance	10 M Ω parallel (3.9 k Ω + 10 i	pF)
Overvoltage protection	±30 V	
Common mode rejection ratio (CMRR) of AISense	> 54 dB, f _{in} < 1kHz	
Channel separation (cross talk)	> 90 dB @ f _{in} 1 kHz	
Transfer characteristics		
DC accuracy		
Range	% of reading	% of range
±10 V	±0.02 %	±0.0115 %
±5 V	±0.02 %	±0.013 %
±2.5 V	±0.02 %	±0.016 %
±1.25 V	±0.02 %	±0.022 %
Gain drift (typ)	±8 ppm/K	
Offset drift (typ)	±5 ppm/K of Range	
Dynamic characteristics		
Signal to noise	89 dB	
THD (f_{in} = 1kHz) 0 dB _{FS} input	< -90 dB	
THD ($f_{in} = 1$ kHz) -20 dB _{FS} input	< -93 dB	
Typical interchannel gain mismatch	±0.015 %	
Inter channel phase mismatch (f _{in} < 500kHz)	0.006° * f _{in} (kHz) + 0.08°	
Maximum working voltage		
Channel-to-ground	10 V, installation category I	
Channel-to-channel	10 V, installation category I	
	,	

4.2 Digital and Counter input

Digital and Counter input	
Counter resolution	32-bit
Counter time base	80 MHz
Time base accuracy	35 ppm
Maximum input frequency	40 MHz
Input signal characteristic main board	
Compatibility	TTL/CMOS
Configuration	Pull-up with 100 kOhm
Input low level	-0.7 V to 0.8 V
Input high level	2 V to 5 V
Input low current	<-50 μA
Input high current	< 10 µA
Input rapacitance	< 5 pF
Overvoltage protection (DI 0 to DI 15)	-1 to 6 V
Input signal characteristic CLK and Trigger	-1.00 V
Compatibility	TTL Schmitt trigger
Configuration	Pull-up with 100 kOhm
Input low level	-0.7 V to 2 V
Input high level	3 V to 5 V
Input low current	<-50 μA
Input low current	< 10 μA
Input rapacitance	< 5 pF
Overvoltage protection	< 5 pr -1 to 6 V
Input signal characteristic expansion board with TTL in	-
Compatibility	TTL/CMOS
Configuration	Pull-up with 100 kOhm
Input low level	-0.7 V to 0.8 V
Input high level	2 V to 5 V
Input low current	< -50 µA
Input high current	< 10 µA
Input capacitance	< 5 pF
Overvoltage protection	±25 V continuous
Input signal characteristic expansion board with adjust	-
Compatibility	Adjustable trigger levels
Configuration	Symmetric differential
Input coupling	DC / AC (1Hz)
Input impedance (ground referenced)	1 MOhm / 5 pF
Bandwith (-3dB)	5 MHz
Trigger adjustment range	0 to 40 V
Trigger resolution	40 mV
Trigger level accuracy	±100 mV ±1% of trigger level
Common voltage range	-35 to 50V
Common mode rejection ratio	>40 dB
Overvoltage protection	±100 V continuous
Max. DC level @AC coupling	±50 V continuous
Input signal characteristic with isolated inputs	
Compatibility	CMOS
Configuration	Isolated input
Input low level	U _{IN} < 1.8 V
Input high level	U _{IN} > 3.2 V
Input high current @ 5 V U _{IN}	< 3.5 mA
Input high current @ 30 V U _{IN}	< 7 mA
Propagation delay	< 160 nsec
Bandwidth	3 MHz
Overvoltage protection	3 MHZ 35 V continuous (65 V peak)
Isolation voltage (channel to channel)	100 V
Isolation voltage (input to output)	250 V
	200 V

4.3 Digital and clock divider output

Digital and clock divider out	
Compatibility	TTL/CMOS
Characteristic	
Low voltage level	< 0.4 V @ 4 mA load
High voltage level	> 3 V @ 4 mA load
Output current	
Sink (low level)	-20 mA
Source (high level)	20 mA
Output impedance	50 Ohm

4.4 CAN interface

CAN interface	
Specification	CAN 2.0B
Physical layer	High speed
Listen only mode	Supported
Galvanic isolation	Not isolated
Bus pin fault protection	±36 V
ESD protection	12 kV (HBM)
CAN tranceiver	SNHVD235
PCI data transfere mode	DMA with SW pooling
ORION-CAN	
5 V DSUB	max. 500 mA per channel (self healing fuse)
12 V DSUB	max. 200 mA for both connectors (self healing fuse)
5 V LEMO	max. 1.1 A (self healing fuse)
12 V LEMO	max. 200 mA (self healing fuse)

4.5 Power requirements

Orion Type	I _{12V} [mA]	I _{5V} [mA]	I _{3.3V} [mA]	P _{tot.} [Watt]
Orion-0816-1000		710	240	4,3
Orion-0816-1001		780	240	4,7
Orion-0816-1002		730	340	4,8
Orion-0816-1003		800	340	5,1
Orion-0816-1004		840	430	5,6
Orion-0816-1005		910	430	6

This table does not include the current taken from the I/O connectors like CAN or analog input.

4.6 General Specifications

General Specifications			
Interface			
PCI	Rev. 3.0		
Transfer Mode	DMA/Scatter-Gather		
Transfer Speed	40 Mbyte/sec max.		
Environmental			
Operating temperature	0 to 50 °C		
Storage temperature	-20 to 70 °C		
Relative humidity	10 to 90%, non condensing		
Maximum altitude	2000 m		
Pollution degree (indoor use only)	2		
Physical			
Dimensions (not including connectors)	17.5 x 10.7 cm (6.9 x 4.2 in.)		
Analog input connector (main board)	68-pin SCSI male (AMP 174341-5)		
Analog input connector (expansion)	68-pin SCSI male (Honda PCS-68LMD)		
Counter input connector (expansion)	68-pin SCSI male (Honda PCS-68LMD)		
CAN input connector			
SUBD	2 x SUB-D 9-pin male		
Lemo	7-pin Lemo connector female (Type: EPG.0B.307.HLN)		

CE-Certificate of conformity

(6

Manufacturer:

Address:

DEWETRON Elektronische Messgeraete Ges.m.b.H.

Parkring 4 A-8074 Graz-Grambach Austria

> Tel.: +43 316 3070 0 Fax: +43 316 3070 90 e-mail: sales@dewetron.com http://www.dewetron.com

Name of product:

Kind of product:

DEWE-ORION-0816-100x A/D board

The product meets the regulations of the following EC-directives:

73/23/EEC

"Directive on the approximation of the laws of the Member States relating to electrical equipment designed for use within certain voltage limits amended by the directive 93/68/EEC"

89/336/EEC

"Directive on the approximation of the laws of the Member States relating to electromagnetic compatibility amended by the directives 91/263/EEC, 92/31/ EEC, 93/68/EEC and 93/97/EEC

The accordance is proved by the observance of the following standards:

L V	Safety	IEC/EN 61010-1:1992/93 IEC/EN 61010-2-031	IEC 61010-1:1992/300 V CATIII Pol. D. 2 IEC 1010-2-031
E	Emissions	EN 61000-6-4	EN 55011 Class B
C	Immunity	EN 61000-6-2	Group standard

Graz, April 28, 2010

Place / Date of the CE-marking

Dipl.-Ing. Roland Jeutter / Managing director

Notes